

RoHS

COMPLIANT HALOGEN

FREE

## **CEM6188-VB** Datasheet

## Dual N-Channel 60-V (D-S) MOSFET

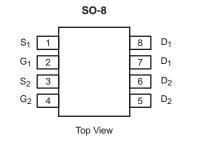
PRODUCT SUMMARY						
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ.)			
60	0.012 at $V_{GS}$ = 10 V	10	15 nC			
60	0.015 at V <sub>GS</sub> = 4.5 V	9.0	15110			

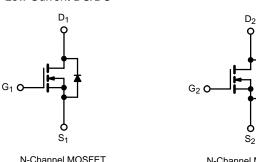
### **FEATURES**

- Halogen-free According to IEC 61249-2-21 • Definition
- TrenchFET<sup>®</sup> Power MOSFET
- 100 % UIS Tested
- 100 % R<sub>g</sub> Tested
- Compliant to RoHS Directive 2002/95/EC

#### **APPLICATIONS**

- Set Top Box
- Low Current DC/DC





N-Channel MOSFET

N-Channel MOSFET

Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		V <sub>DS</sub>	60	V	
Gate-Source Voltage		V <sub>GS</sub>	± 20	v	
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>C</sub> = 25 °C T <sub>C</sub> = 70 °C	_	10 <sup>a</sup> 8.2		
	$T_{A} = 25 \text{ °C}$	I <sub>D</sub>	7.2 <sup>b, c</sup>		
Pulsed Drain Current	T <sub>A</sub> = 70 °C		6.2 <sup>b, c</sup> 40	Α	
	T <sub>C</sub> = 25 °C	IDM	2.25		
Continuous Source-Drain Diode Current	$T_{A} = 25 \text{ °C}$	ا <sub>S</sub>	1.48 <sup>b, c</sup>		
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	30		
L = 0.1 mH		E <sub>AS</sub>	55	mJ	
	T <sub>C</sub> = 25 °C		2.7		
Maximum Power Dissipation	T <sub>C</sub> = 70 °C	PD	4.77	w	
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	'D	2.78 <sup>b, c</sup>	vv	
	T <sub>A</sub> = 70 °C		1.14 <sup>b, c</sup>		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Typical	Maximum	Unit			
Maximum Junction-to-Ambient <sup>a, c, d</sup>	t ≤ 10 s	R <sub>thJA</sub>	58	70	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	38	45	- C/W		

Notes:

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a. Package limited,  $T_C = 25 \text{ °C}$ . b. Surface Mounted on 1" x 1" FR4 board.

d. Maximum under Steady State conditions is 110 °C/W.

服务热线:400-655-8788

c. t = 10 s.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static						•	
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \mu\text{A}$	60			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	L _ 250 uA		32		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μA		- 5.0			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$	1.0		2.5	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
	I <sub>DSS</sub> -	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	μA	
Zero Gate Voltage Drain Current		V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			10		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5$ V, $V_{GS}$ = 10 V	10			Α	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5 A		0.012			
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 4 A		0.015		Ω	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	$V_{DS} = 10 \text{ V}, I_D = 5 \text{ A}$		16		S	
Dynamic <sup>b</sup>	<u> </u>						
Input Capacitance	C <sub>iss</sub>			586		pF	
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 V, f = 1 MHz		217			
Reverse Transfer Capacitance	C <sub>rss</sub>			155			
Total Gate Charge	Q <sub>g</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 5 \text{ A}$		15	25	nC	
				6.7	9.6		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = 15 V, $V_{GS}$ = 4.5 V, $I_{D}$ = 5 A		4.4	6.0		
Gate-Drain Charge	Q <sub>gd</sub>			3.05	4.0		
Gate Resistance	R <sub>g</sub>	f = 1 MHz	0.8	4.3	8.6	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			12	24	ns	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 3 $\Omega$		55	100		
Turn-Off Delay Time	t <sub>d(off)</sub>	$\text{I}_\text{D} \cong \text{5}$ A, $\text{V}_\text{GEN}$ = 4.5 V, $\text{R}_\text{g}$ = 1 $\Omega$		11	22		
Fall Time	t <sub>f</sub>			8	16		
Turn-On Delay Time	t <sub>d(on)</sub>			4	8		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 3 $\Omega$		9	18		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 5$ A, $V_{GEN}$ = 10 V, $R_g$ = 1 $\Omega$		10	20		
Fall Time	t <sub>f</sub>			6	12		
Drain-Source Body Diode Characteristi	cs						
Continuous Source-Drain Diode Current	۱ <sub>S</sub>	T <sub>C</sub> = 25 °C			2.25		
Pulse Diode Forward Current	I <sub>SM</sub>				24	A	
Body Diode Voltage	V <sub>SD</sub>	$I_{S} = 2 \text{ A}, V_{GS} = 0 \text{ V}$		0.8	1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			11	20	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			4	8	nC	
Reverse Recovery Fall Time	ta	$I_F = 5 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, \text{ T}_J = 25 ^\circ\text{C}$		7			
Reverse Recovery Rise Time	t <sub>b</sub>			4		ns	

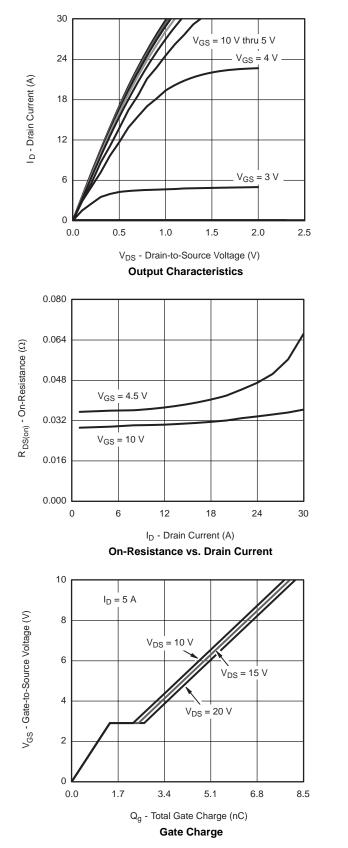
Notes:

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %

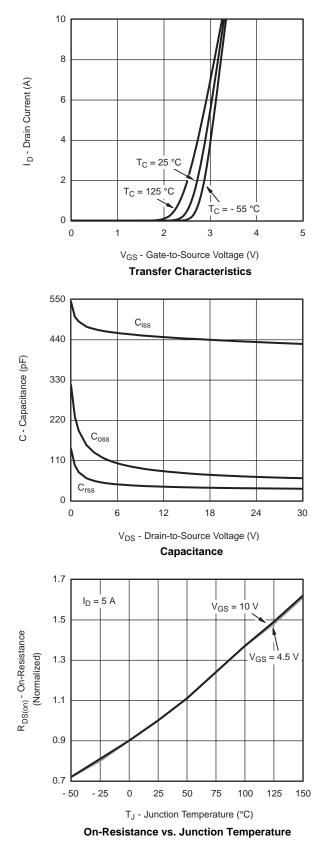
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

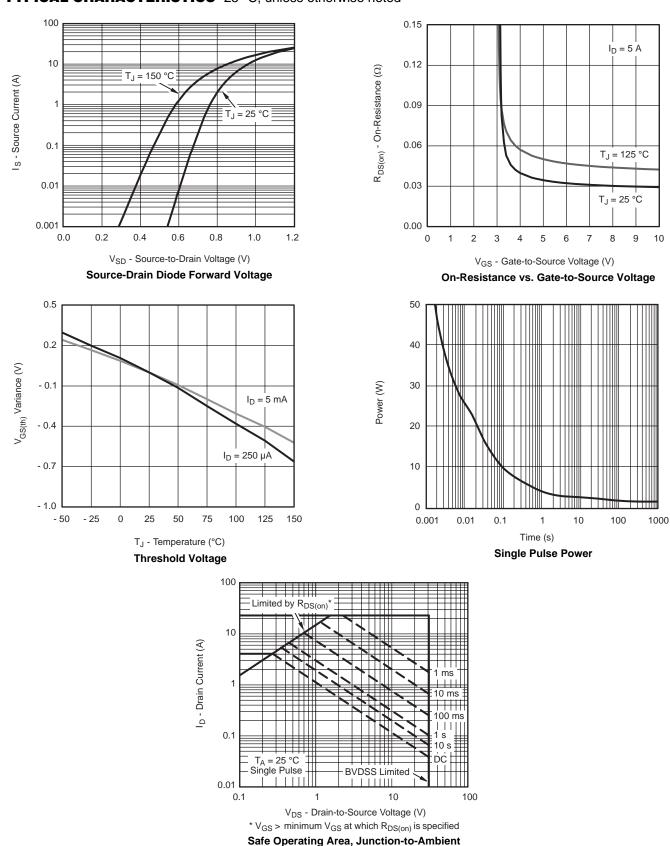




#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



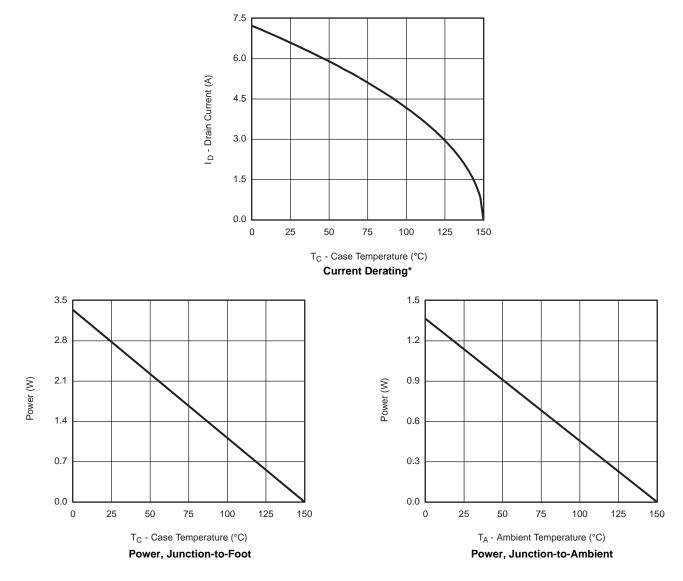




#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



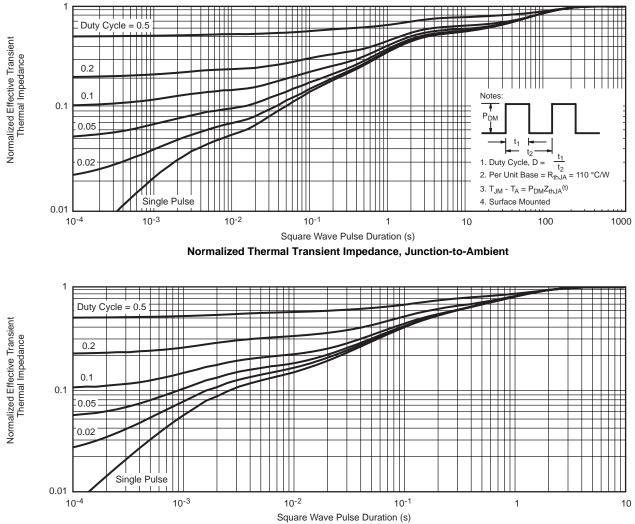
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



\* The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.







Normalized Thermal Transient Impedance, Junction-to-Foot



# SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012

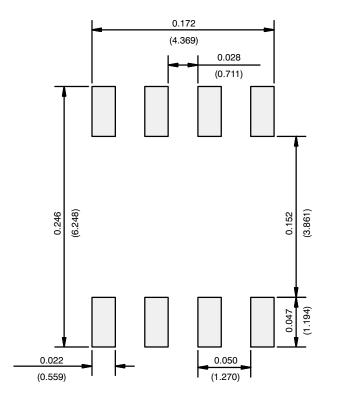




	MILLIMETERS		INC	HES		
DIM	Min	Мах	Min	Max		
A	1.35	1.75	0.053	0.069		
A <sub>1</sub>	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
E	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050	0.050 BSC		
Н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498						



### **RECOMMENDED MINIMUM PADS FOR SO-8**



Recommended Minimum Pads Dimensions in Inches/(mm)

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