

Part Number : 387006110

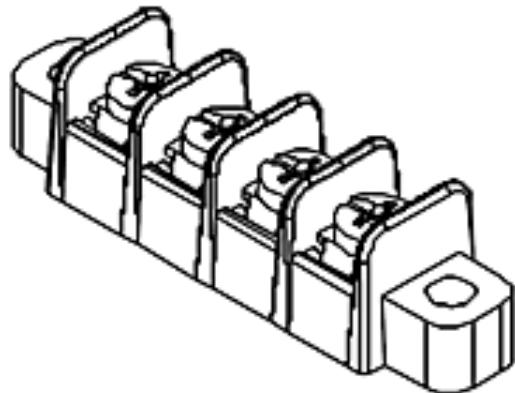
Product Description : 8.26mm Pitch Beau PCB Terminal Strip, with Mounting Ends, 10 Circuits

Series Number : 38700

Status : Active

Product Category : Terminal Blocks and Barrier Strip

Engineering Number : 70510



## Documents & Resources

### Drawings

Drawing 387006110\_sd.pdf

### 3D Models and Design Files

3D Model 387006110\_stp.zip

## Product Environment Compliance

### Compliance

GADSL/IMDS	Not Relevant
China RoHS	
EU ELV	Not Relevant
Low-Halogen Status	Low-Halogen per IEC 61249-2-21
REACH SVHC	Not Contained per D(2023)3788-DC (14 Jun 2023)
EU RoHS	Compliant per EU 2015/863

### Multiple Part Product Compliance Statements

- Eu RoHS
- REACH SVHC
- Low-Halogen

### Multiple Part Industry Compliance Documents

- IPC 1752A Class C
- IPC 1752A Class D
- Molex Product Compliance Declaration

- IEC-62474
- chemSHERPA (xml)

## EU RoHS Certificate of Compliance

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### Part Details

#### General

Status	Active
Category	Terminal Blocks and Barrier Strip
Series	38700
Description	8.26mm Pitch Beau PCB Terminal Strip, with Mounting Ends, 10 Circuits
Application	N/A
Component Type	One Piece
Product Family	Beau Barrier Strips
Product Name	Fixed Mount Barrier
Type	Barrier Strip
UPC	800756312828

#### Electrical

Current - Maximum per Contact	15.0A
Voltage - Maximum	300V

#### Physical

Circuits (Loaded)	10
Circuits (maximum)	10
Entry Angle	Horizontal
Lock to Mating Part	None
Material - Metal	Brass
Material - Plating Mating	Tin
Material - Plating Termination	Tin
Net Weight	20.474/g
Number of Rows	1
Orientation	Horizontal
Panel Mount	No

PC Tail Length	3.56mm
PCB Retention	Yes
Pitch - Mating Interface	8.26mm
Pitch - Termination Interface	8.26mm
Plating min - Mating	3.810 $\mu$ m
Plating min - Termination	3.810 $\mu$ m
Polarized to Mating Part	No
Shrouded	Dual-Barrier
Stackable	Yes
Temperature Range - Operating	-40° to +130°C
Termination Interface Style	Through Hole
Wire Size (AWG)	14, 16, 18, 20, 22
Wire Size mm <sup>2</sup>	0.50-1.50

## Solder Process Data

Lead-Free Process Capability	WAVE
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This document was generated on Jan 24, 2024