

## **AO4443-VB Datasheet**

# P-Channel 40 V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)			
- 40	0.010 at V <sub>GS</sub> = - 10 V	- 16.1	33 nC			
- 40	$0.014$ at $V_{GS} = -4.5 \text{ V}$	- 13.3	33110			

## **FEATURES**

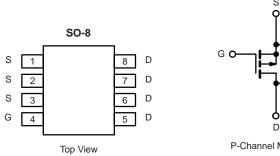
- Halogen-free According to IEC 61249-2-21 Definition
- 100 % R<sub>g</sub> Tested
- 100 % UIS Tested
- Compliant to RoHS Directive 2002/95/EC





#### **APPLICATIONS**

- Load Switch
- POL



	D Channel MOCETT
View	P-Channel MOSFET

Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		V <sub>DS</sub>	- 40	V	
Gate-Source Voltage		V <sub>GS</sub>	± 20	V	
	T <sub>C</sub> = 25 °C		- 16.1		
Continuous Dunis Courset /T 450 °C)	T <sub>C</sub> = 70 °C	1 .	- 12.9		
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>A</sub> = 25 °C	I <sub>D</sub>	- 10.2 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C		- 8.2 <sup>b, c</sup>	^	
Pulsed Drain Current		I <sub>DM</sub>	- 50	A	
Continous Source-Drain Diode Current	T <sub>C</sub> = 25 °C		- 5.3		
Continous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	ls -	- 2.1 <sup>b, c</sup>		
Single Pulse Avalanche Current		I <sub>AS</sub>	- 28		
Single Pulse Avalanche Energy  L = 0.1 mH		E <sub>AS</sub>	39	mJ	
	T <sub>C</sub> = 25 °C		6.3		
Maximum Dawar Dissination	T <sub>C</sub> = 70 °C		4	W	
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	2.5 <sup>b, c</sup>	VV	
	T <sub>A</sub> = 70 °C		1.6 <sup>b, c</sup>		
Operating Junction and Storage Temperature	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient <sup>b, d</sup>	t ≤ 10 s	R <sub>thJA</sub>	37	50	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	16	20	- C/VV		

#### Notes:

- a. Based on T<sub>C</sub> = 25 °C.
  b. Surface mounted on 1" x 1" FR4 board.
- d. Maximum under steady state conditions is 85 °C/W.



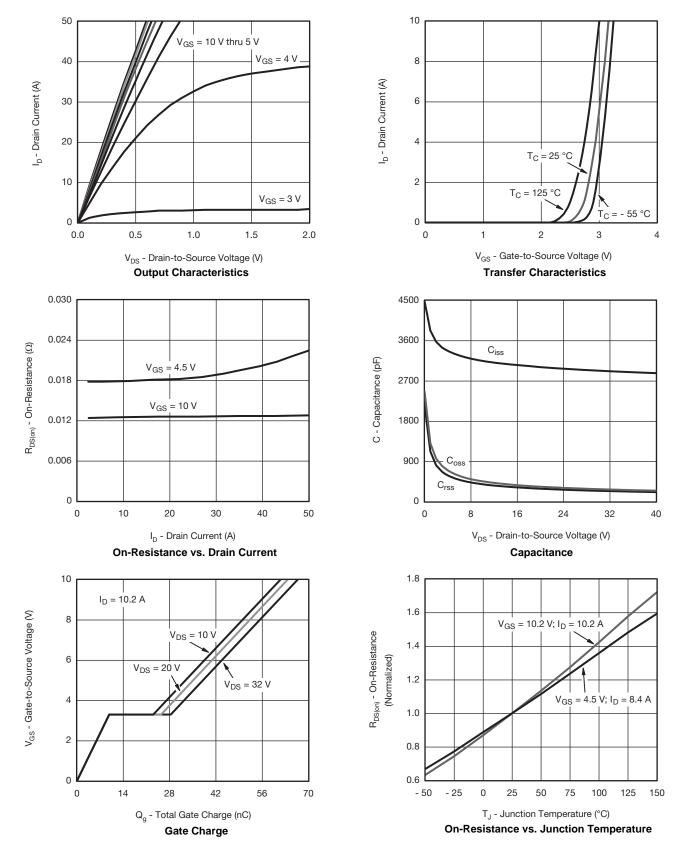
Parameter   Symbol   Test Conditions   Min.   Typ.   Max.   Unit Static	<b>SPECIFICATIONS</b> T <sub>J</sub> = 25 °C, unless otherwise noted								
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
V <sub>DS</sub> Temperature Coefficient         ΔV <sub>DS</sub> /T <sub>J</sub> V <sub>OS(P)</sub> Temperature Coefficient         ΔV <sub>DS(P)</sub> To V <sub>DS(P)</sub> Temperature Coefficient         ΔV <sub>DS(P)</sub> To V <sub>DS(DS(P)</sub> To V <sub>DS(DS(P)</sub> To V <sub>DS(DS(P)</sub> To V <sub>DS(DS(DS(DS(P))</sub> To V <sub>DS(DS(DS(DS(DS(DS(DS(DS(DS(DS(DS(DS(DS(D</sub>	Static								
Vosigin   Temperature Coefficient   ΔV <sub>GS(th)</sub>	Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = - 250 μA	- 40			V		
V <sub>OSCHI</sub> , Temperature Coefficient         Λ/V <sub>OSCHI</sub> /T <sub>J</sub> V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = ·250 μA         -1.2         S         V         V         V         V <sub>DS</sub> = V <sub>S</sub> , I <sub>D</sub> = ·250 μA         -1.2         -2.5         V         A         -2.5         V         A         -2.5         V         A	V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	J. 250A		- 36		m\//°C		
Sate-Source Leakage   1 <sub>GSS</sub>   V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V   ± 100   nA	V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	η ΙΔ = - 250 μΑ		5		IIIV/C		
Seary   Sea	Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = -250 \mu A$	- 1.2		- 2.5	V		
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	Gate-Source Leakage		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA		
On-State Drain Current³         I <sub>D(on)</sub> V <sub>DS</sub> = -40 V, V <sub>GS</sub> = 0 V, I <sub>J</sub> = 56 °C         -5           On-State Drain Current³         I <sub>D(on)</sub> V <sub>DS</sub> = -5 V, V <sub>GS</sub> = -10 V, I <sub>D</sub> = -10.2 A         0.010         Ω           Drain-Source On-State Resistance³         R <sub>DS(on)</sub> V <sub>GS</sub> = -10 V, I <sub>D</sub> = -10.2 A         0.010         Ω           Forward Transconductance³         9 fs         V <sub>DS</sub> = -15 V, I <sub>D</sub> = -10.2 A         37         S           Dynamic³           Input Capacitance         C <sub>ISS</sub> V <sub>DS</sub> = -10 V, I <sub>D</sub> = -10.2 A         3007         P           Output Capacitance         C <sub>ISS</sub> V <sub>DS</sub> = -20 V, V <sub>GS</sub> = 0 V, f = 1 MHz         3007         PF           Everse Transfer Capacitance         C <sub>ISS</sub> V <sub>DS</sub> = -20 V, V <sub>GS</sub> = -10 V, I <sub>D</sub> = -10.2 A         64         95           Gate-Drain Charge         Q <sub>g</sub> V <sub>DS</sub> = -20 V, V <sub>GS</sub> = -10 V, I <sub>D</sub> = -10.2 A         64         95         33         50           Gate-Drain Charge         Q <sub>g</sub> V <sub>DS</sub> = -20 V, V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -10.2 A         64         95         9.8         15         15         16         15.7         10         16         15.7         10         16         15.7         10         16         15.7         10         16         16	Zana Cata Valta na Duain Commant		V <sub>DS</sub> = - 40 V, V <sub>GS</sub> = 0 V			- 1			
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Zero Gate Voltage Drain Current	DSS	V <sub>DS</sub> = - 40 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 5	μΑ		
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le -5 \text{ V}, V_{GS} = -10 \text{ V}$	- 25			Α		
Promate Transconductance   Promate Transcorductance   Promate Transcord	Dunin Course On Chata Basistanas	В	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 10.2 A		0.010		Ω		
Dynamic <sup>b</sup> Input Capacitance         C <sub>Iss</sub> V <sub>DS</sub> = -20 V, V <sub>GS</sub> = 0 V, f = 1 MHz         3007         PF           Output Capacitance         C <sub>Oss</sub> V <sub>DS</sub> = -20 V, V <sub>GS</sub> = 0 V, f = 1 MHz         335         pF           Reverse Transfer Capacitance         C <sub>rss</sub> 291         291           Total Gate Charge         Q <sub>g</sub> V <sub>DS</sub> = -20 V, V <sub>GS</sub> = -10 V, I <sub>D</sub> = -10.2 A         64         95           Gate-Source Charge         Q <sub>gs</sub> V <sub>DS</sub> = -20 V, V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -10.2 A         9.8         nc           Gate-Source Charge         Q <sub>gs</sub> V <sub>DS</sub> = -20 V, V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -10.2 A         9.8         nc           Gate-Drain Charge         Q <sub>gs</sub> V <sub>DS</sub> = -20 V, V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -10.2 A         9.8         nc           Gate-Brain Charge         Q <sub>gs</sub> f = 1 MHz         0.4         2         4         Ω           Turn-On Delay Time         t <sub>d(ofn)</sub> I <sub>D</sub> = -8.2 A, V <sub>GEN</sub> = -4.5 V, R <sub>g</sub> = 1 Ω         50         75         86           Fall Time         t <sub>t</sub> V <sub>DD</sub> = -20 V, R <sub>L</sub> = 2.4 Ω         11         20         11         12         11         20         11         12         12         11         20         12         12         11         12         12	Drain-Source On-State Resistance <sup>4</sup>	KDS(on)	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 8.4 A		0.014				
$ \begin{array}{ c c c c c c c } \hline \mbox{lnput Capacitance} & C_{iss} \\ \hline \mbox{Output Capacitance} & C_{oss} \\ \hline \mbox{Output Capacitance} & C_{oss} \\ \hline \mbox{Reverse Transfer Capacitance} & C_{rss} \\ \hline \mbox{Total Gate Charge} & Q_g \\ \hline \mbox{Gate-Source Charge} & Q_gs \\ \hline \mbox{Gate-Drain Charge} & Q_gd \\ \hline \mbox{Gate-Box Minimal Charge} & Q_gd \\ \hline \mbox{Gate-Box Minimal Charge} & Q_gd \\ \hline \mbox{Gate-Drain Charge} & Q_gd \\ \hline \mbox$	Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 10.2 A		37		S		
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	Dynamic <sup>b</sup>				•	•			
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	Input Capacitance	C <sub>iss</sub>			3007				
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	Output Capacitance	C <sub>oss</sub>	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		335		pF		
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	Reverse Transfer Capacitance	C <sub>rss</sub>			291				
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	Total Cata Charga		V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 10.2 A		64	95	nC		
Gate-Source Charge $Q_{gs}$ $V_{DS} = -20 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -10.2 \text{ A}$ 9.8           Gate Porain Charge $Q_{gd}$ 15.7         15.7           Gate Resistance $R_g$ $f = 1 \text{ MHz}$ 0.4         2         4         Ω           Turn-On Delay Time $t_{d(on)}$ $V_{DD} = -20 \text{ V}, R_L = 2.4 \Omega$ 50         75         86           Fall Time $t_f$ $V_{DD} = -20 \text{ V}, R_L = 2.4 \Omega$ 40         60         60           Fall Time $t_f$ $V_{DD} = -20 \text{ V}, R_L = 2.4 \Omega$ 11         20         11         20           Rise Time $t_f$ $V_{DD} = -20 \text{ V}, R_L = 2.4 \Omega$ 11         20         11         20         11         20         11         20         12         12         45         68	Total Gate Charge				33	50			
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	Gate-Source Charge	$Q_{gs}$	$V_{DS} = -20 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -10.2 \text{ A}$		9.8				
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	Gate-Drain Charge	$Q_{gd}$			15.7				
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	Gate Resistance	$R_g$	f = 1 MHz	0.4	2	4	Ω		
Turn-Off Delay Time $t_{d(off)}$ $I_D \cong -8.2 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$ 40         60           Fall Time $t_f$ 17         26           Turn-On Delay Time $t_{d(on)}$ 13         20           Rise Time $t_r$ $V_{DD} = -20 \text{ V}, R_L = 2.4 \Omega$ 11         20           Turn-Off Delay Time $t_d(off)$ $I_D \cong -8.2 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$ 45         68           Fall Time $t_f$ 9         18           Drain-Source Body Diode Characteristics           Continuous Source-Drain Diode Current $I_S$ $T_C = 25 \text{ °C}$ -5.3         A           Pulse Diode Forward Current $I_{SM}$ -50         -50         A           Body Diode Voltage $V_{SD}$ $I_S = -8.2 \text{ A}, V_{GS} = 0 \text{ V}$ -0.8         -1.2         V           Body Diode Reverse Recovery Time $t_{rr}$ $I_F = -8.2 \text{ A}, dI/dt = 100 \text{ A}/\mu_S, T_J = 25 °C$ 41         62         nC           Reverse Recovery Fall Time $t_a$ $t_a$ $t_a$ $t_a$ $t_a$ $t_a$ $t_a$ $t_a$	Turn-On Delay Time	t <sub>d(on)</sub>			57	86			
Fall Time $t_f$ 17         26           Turn-On Delay Time $t_{d(on)}$ 13         20           Rise Time $t_r$ $V_{DD} = -20 \text{ V}, R_L = 2.4 \Omega$ 11         20           Turn-Off Delay Time $t_{d(off)}$ $I_D \cong -8.2 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$ 45         68           Fall Time $t_f$ 9         18           Drain-Source Body Diode Characteristics           Continuous Source-Drain Diode Current $I_S$ $T_C = 25 \text{ °C}$ -5.3         A           Pulse Diode Forward Current $I_{SM}$ -50         -50         A           Body Diode Voltage $V_{SD}$ $I_S = -8.2 \text{ A}, V_{GS} = 0 \text{ V}$ -0.8         -1.2         V           Body Diode Reverse Recovery Time $t_{rr}$ 36         54         ns           Body Diode Reverse Recovery Charge $Q_{rr}$ $I_F = -8.2 \text{ A}, \text{ dl/dt} = 100 \text{ A/μs}, T_J = 25 \text{ °C}$ 41         62         nC           Reverse Recovery Fall Time $I_A$ 20	Rise Time	t <sub>r</sub>	$V_{DD}$ = - 20 V, $R_L$ = 2.4 $\Omega$		50	75			
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong -8.2 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$		40	60			
Turn-On Delay Time $t_{d(on)}$ Rise Time $t_r$ $V_{DD} = -20 \text{ V}, R_L = 2.4 \Omega$ $I_D \cong -8.2 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$ $Drain-Source Body Diode Characteristics$ $Continuous Source-Drain Diode Current$ $I_S$ $Drain-Source Body Diode Characteristics$ $Continuous Source-Drain Diode Current$ $I_{SM}$ $I_{SM}$ $I_{SM}$ $I_{S} = -8.2 \text{ A}, V_{GS} = 0 \text{ V}$	Fall Time	t <sub>f</sub>			17	26			
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	Turn-On Delay Time	t <sub>d(on)</sub>			13	20	115		
Fall Time $t_f$ $9$ $18$ Drain-Source Body Diode Characteristics  Continuous Source-Drain Diode Current $t_S$ $t_C = 25 ^{\circ}\text{C}$ $t$	Rise Time	t <sub>r</sub>	$V_{DD}$ = - 20 V, $R_L$ = 2.4 $\Omega$		11	20			
	Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong$ - 8.2 A, $V_{GEN}$ = - 10 V, $R_g$ = 1 $\Omega$		45	68			
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	Fall Time	t <sub>f</sub>			9	18			
Pulse Diode Forward Current $I_{SM}$ $-50$ Body Diode Voltage $V_{SD}$ $I_S = -8.2 \text{ A}, V_{GS} = 0 \text{ V}$ $-0.8 -1.2 \text{ V}$ Body Diode Reverse Recovery Time $t_{rr}$ $36$ $54$ $ns$ Body Diode Reverse Recovery Charge $Q_{rr}$ $I_F = -8.2 \text{ A}, dI/dt = 100 \text{ A/µs}, T_J = 25 °C$ Reverse Recovery Fall Time $t_a$ $Q_{rr}$	Drain-Source Body Diode Characteristics								
Pulse Diode Forward Current $I_{SM}$ $-50$ Body Diode Voltage $V_{SD}$ $I_S = -8.2 \text{ A}, V_{GS} = 0 \text{ V}$ $-0.8 -1.2 \text{ V}$ Body Diode Reverse Recovery Time $t_{rr}$ $36 54 \text{ ns}$ Body Diode Reverse Recovery Charge $Q_{rr}$ Reverse Recovery Fall Time $t_a$ $I_F = -8.2 \text{ A}, \text{ dl/dt} = 100 \text{ A/µs}, T_J = 25 °C$ $Q_{rr}$	Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			- 5.3	Δ		
Body Diode Reverse Recovery Time $t_{rr}$ 36 54 ns Body Diode Reverse Recovery Charge $Q_{rr}$ $I_F = -8.2 \text{ A}, \text{ dI/dt} = 100 \text{ A/µs}, T_J = 25 °C$ 41 62 nC Reverse Recovery Fall Time $t_a$	Pulse Diode Forward Current					- 50			
Body Diode Reverse Recovery Charge $Q_{rr}$ $I_F = -8.2 \text{ A}, \text{ dI/dt} = 100 \text{ A/µs}, T_J = 25 °C $ 41 62 nC Reverse Recovery Fall Time $t_a$	Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = -8.2 A, V <sub>GS</sub> = 0 V		- 0.8	- 1.2	V		
Reverse Recovery Fall Time $t_a$ $I_F = -8.2 \text{ A}, \text{ dl/dt} = 100 \text{ A/µs}, I_J = 25 \text{ °C}$ 20	Body Diode Reverse Recovery Time	t <sub>rr</sub>			36	54	ns		
Reverse Recovery Fall Time   t <sub>a</sub>   20   20	Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			41	62	nC		
	Reverse Recovery Fall Time	t <sub>a</sub>	i <sub>F</sub> = -0.2 Λ, απαι = 100 Αγμο, 1 <sub>J</sub> = 25 °C		20		ns		
Reverse Recovery Rise Time t <sub>b</sub>	Reverse Recovery Rise Time	t <sub>b</sub>			16				

#### Notes:

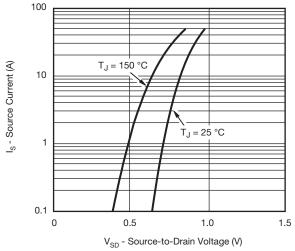
- a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %. b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

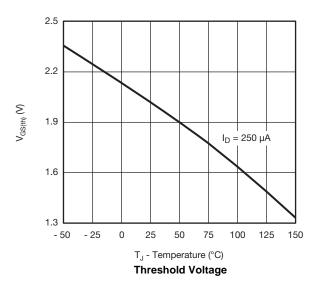






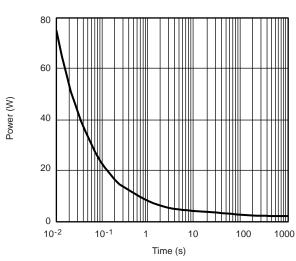


#### Source-Drain Diode Forward Voltage

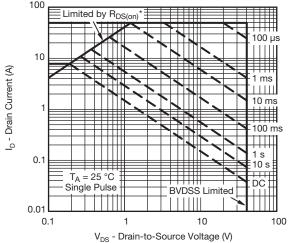


 $C_{\text{O}} = 10.2 \text{ A}$   $C_{\text$ 

On-Resistance vs. Gate-to-Source Voltage



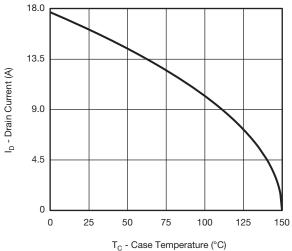
Single Pulse Power (Junction-to-Ambient)



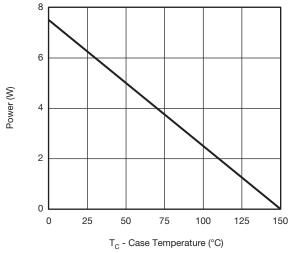
 $^{*}V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

Safe Operating Area, Junction-to-Ambient

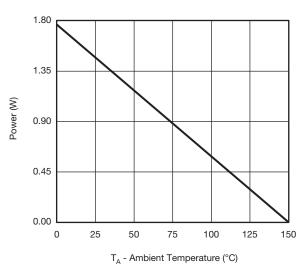




#### **Current Derating\***



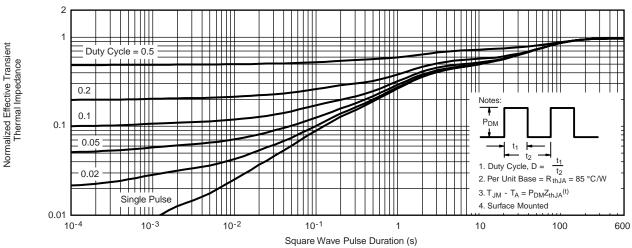




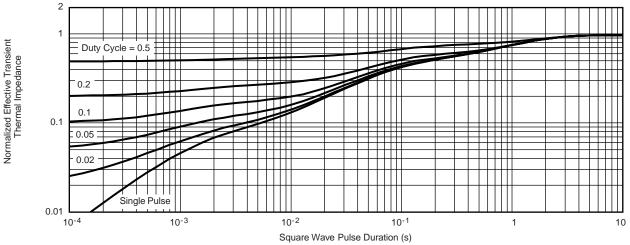
Power, Junction-to-Ambient

 $<sup>^*</sup>$  The power dissipation  $P_D$  is based on  $T_{J(max)}$  = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





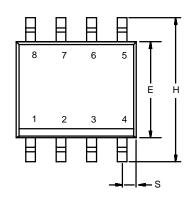


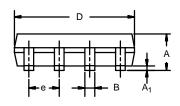


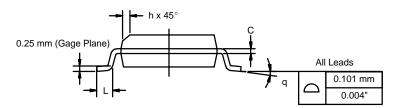
Normalized Thermal Transient Impedance, Junction-to-Foot



**SOIC (NARROW): 8-LEAD** JEDEC Part Number: MS-012





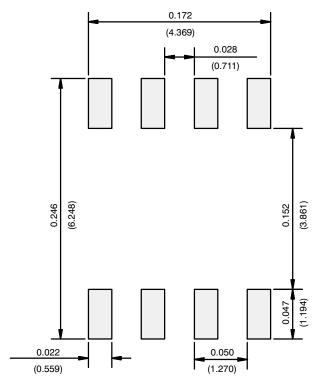


	MILLIMETERS		INCHES		
DIM	Min	Max	Min	Max	
Α	1.35	1.75	0.053	0.069	
A <sub>1</sub>	0.10	0.20	0.004	0.008	
В	0.35	0.51	0.014	0.020	
С	0.19	0.25	0.0075	0.010	
D	4.80	5.00	0.189	0.196	
Е	3.80	4.00	0.150	0.157	
е	1.27 BSC		0.050 BSC		
Н	5.80	6.20	0.228	0.244	
h	0.25	0.50	0.010	0.020	
L	0.50	0.93	0.020	0.037	
q	0°	8°	0°	8°	
S	0.44	0.64	0.018	0.026	
FCN: C-06527-Rev. I. 11-Sep-06					

ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498



#### **RECOMMENDED MINIMUM PADS FOR SO-8**



Recommended Minimum Pads Dimensions in Inches/(mm)



# **Disclaimer**

All products due to improve reliability, function or design or for other reasons, product specifications and data are subject to change without notice.

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