

RoHS

COMPLIANT HALOGEN

FREE

AM2308N-T1-PF-VB Datasheet

N-Channel 30-V (D-S) MOSFET

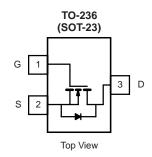
PRODUCT SUMMARY						
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^a	Q _g (Typ.)			
30	0.030 at V _{GS} = 10 V	6.5	4.5 nC			
	0.033 at V _{GS} = 4.5 V	6.0	4.5 110			

FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFET
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

DC/DC Converter





ABSOLUTE MAXIMUM RATIN	IIII = 25 C,				
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	30	V	
Gate-Source Voltage		V _{GS}	± 20	v	
	T _C = 25 °C		6.5 ^a		
Continuous Drain Current ($T_1 = 150 \ ^{\circ}C$)	T _C = 70 °C	I _D	6.0		
	T _A = 25 °C		5.3		
	T _A = 70 °C		5.0	A	
Pulsed Drain Current		I _{DM}	25		
Continuous Source-Drain Diode Current	T _C = 25 °C		1.4	1	
	T _A = 25 °C	۱ _S	0.9 ^{b, c}		
	T _C = 25 °C		1.7		
Maximum Power Dissipation	T _C = 70 °C	P _D	1.1	w	
	T _A = 25 °C	۲D	1.1 ^{b, c}	- vv	
	T _A = 70 °C		0.7 ^{b, c}		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	
Soldering Recommendations (Peak Temperature) ^{d, e}			260	- °C	

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^{b, d}	t ≤ 5 s	R _{thJA}	90	115	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	60	75	0/11		

Notes:

a. Package limited

b. Surface Mounted on 1" x 1" FR4 board.

c. t = 5 s.

d. Maximum under steady state conditions is 130 °C/W.

SPECIFICATIONS $T_J = 25 \text{ °C}$, Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static	Cymbol			199.	max.	01110
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	30			V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$			31		- mV/°(
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J	I _D = 250 μA		- 5		
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_{D} = 250 \ \mu A$	0.7	1.1	2.0	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
	000	$V_{\rm DS} = 30 \text{ V}, \text{ V}_{\rm GS} = 0 \text{ V}$			1	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55 \text{ °C}$			10	μA
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 V, V_{GS} = 10 V$	10			Α
		V _{GS} = 10 V, I _D = 3.2 A	D _D = 3.2 A 0.030			Ω
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 2.8 \text{ A}$				
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 4.8 A		11		S
Dynamic ^b				I	I	
Input Capacitance	C _{iss}			335		[
Output Capacitance	C _{oss}	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz		45		pF
Reverse Transfer Capacitance	C _{rss}			17		
		$V_{DS} = 15 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 3.4 \text{ A}$		4.5	6.7	- nC
Total Gate Charge	Q _g			2.1	3.2	
Gate-Source Charge	Q _{gs}	V_{DS} = 15 V, V_{GS} = 4.5 V, I_{D} = 3.4 A		0.85		
Gate-Drain Charge	Q _{gd}			0.65		
Gate Resistance	Rg	f = 1 MHz	0.8	4.4	8.8	Ω
Turn-On Delay Time	t _{d(on)}			12	20	
Rise Time	t _r	V_{DD} = 15 V, R_{L} = 5.6 Ω		50	75	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 2.7$ A, V_{GEN} = 4.5 V, R_g = 1 Ω		12	20	
Fall Time	t _f			22	35	
Turn-On Delay Time	t _{d(on)}			5	10	ns
Rise Time	t _r	V_{DD} = 15 V, R_L = 5.6 Ω		12	20	_
Turn-Off Delay Time	t _{d(off)}	$\text{I}_{\text{D}}\cong$ 2.7 A, V_{GEN} = 10 V, R_{g} = 1 Ω		10	15	
Fall Time	t _f			5	10	
Drain-Source Body Diode Characteristi	cs				•	•
Continuous Source-Drain Diode Current	۱ _S	$T_{C} = 25 \ ^{\circ}C$			1.4	A
Pulse Diode Forward Current	I _{SM}				15	
Body Diode Voltage	V _{SD}	$I_{S} = 2.7 \text{ A}, V_{GS} = 0 \text{ V}$		0.8	1.2	V
Body Diode Reverse Recovery Time	t _{rr}			10	20	ns
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = 2.7 A, dl/dt = 100 A/µs, T _{.1} = 25 °C		5	10	nC
Reverse Recovery Fall Time	ta	F = 2.7 A, unut = 100 A/ µs, 1 J = 25 C		6		
Reverse Recovery Rise Time	t _b			4		ns

Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %

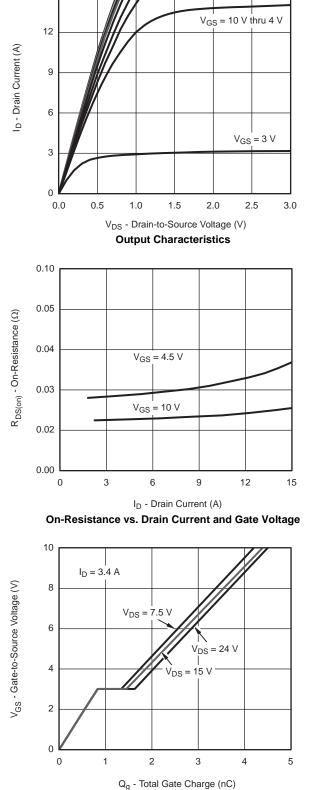
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

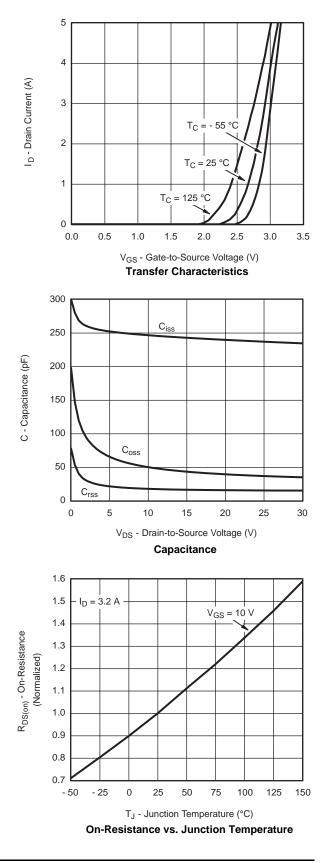
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



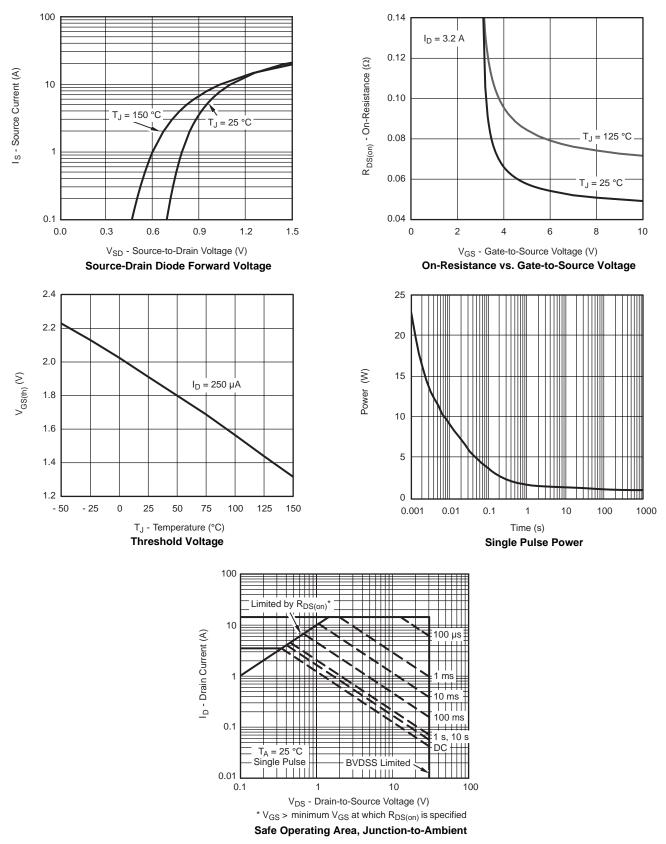
Gate Charge



服务热线:400-655-8788



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





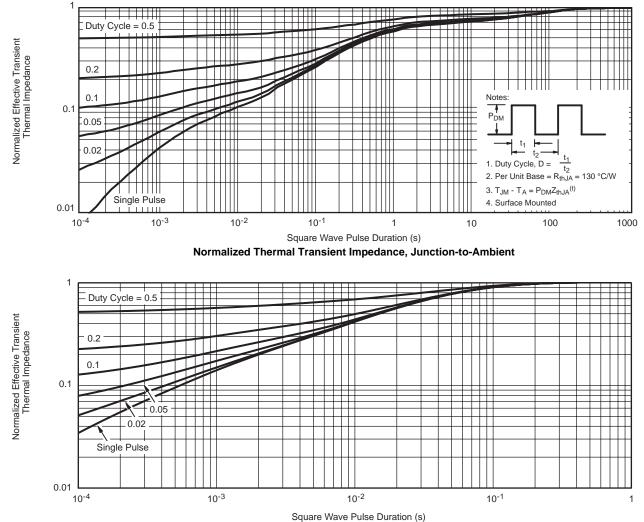
6 2.0 5 1.5 I_D - Drain Current (A) Package Limited 4 Power (W 1.0 3 0.5 2 0 0.0 25 75 150 50 75 150 0 50 100 125 25 100 125 T_C - Case Temperature (°C) T_C - Case Temperature (°C) **Current Derating* Power Derating**

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

* The power dissipation P_D is based on $T_{J(max)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot



SOT-23 (TO-236): 3-LEAD







		NETERS	INCHES			
Dim	Min	Max	Min	Мах		
Α	0.89	1.12	0.035	0.044		
A ₁	0.01	0.10	0.0004	0.004		
A ₂	0.88	1.02	0.0346	0.040		
b	0.35	0.50	0.014	0.020		
C	0.085	0.18	0.003	0.007		
D	2.80	3.04	0.110	0.120		
E	2.10	2.64	0.083	0.104		
E ₁	1.20	1.40	0.047	0.055		
е	0.95 BSC		0.0374 Ref			
e ₁	1.90 BSC		0.0748 Ref			
L	0.40	0.60	0.016	0.024		
L ₁	0.64 Ref		0.025	0.025 Ref		
S	0.50 Ref		0.020 Ref			
q	3°	8°	3°	8°		



RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

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