

SQD40P10-40L-GE3-VB Datasheet

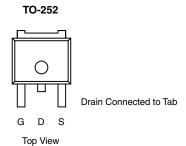
P-Channel 100 V (D-S) MOSFET

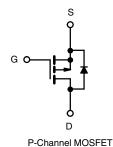
PRODUCT SUMMARY					
V _{DS} (V)	- 100				
$R_{DS(on)}(\Omega)$ at $V_{GS} = -10 \text{ V}$	0.033				
$R_{DS(on)}(\Omega)$ at $V_{GS} = -4.5 \text{ V}$	0.036				
I _D (A)	- 40				
Configuration	Single				

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- Package with Low Thermal Resistance
- \bullet 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2002/95/EC







ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	LIMIT	UNIT			
Drain-Source Voltage	V_{DS}	- 100				
Gate-Source Voltage	V _{GS}	± 20	V			
Continuous Drain Current	T _C = 25 °C	1	- 40			
Continuous Drain Current	T _C = 125 °C	- I _D	- 22			
Continuous Source Current (Diode Conduction) ^a	I _S	- 50	А			
Pulsed Drain Current ^b	I _{DM}	- 150				
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	- 44			
Single Pulse Avalanche Energy	L = U. I MH	E _{AS}	96	mJ		
Maximum Power Dissipation ^b	T _C = 25 °C	- P _D	136	W		
	T _C = 125 °C		45	VV		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to + 175	°C		

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	LIMIT	UNIT		
Junction-to-Ambient	PCB Mount ^c	R_{thJA}	50	°C/W		
Junction-to-Case (Drain)		R _{thJC}	1.1	C/ VV		

Notes

- a. Package limited.
- b. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%.$
- c. When mounted on 1" square PCB (FR-4 material).
- d. Parametric verification ongoing.

服务热线:400-655-8788

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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static		•				L	
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$		- 100	-	-	V
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_D = -250 \mu A$		-	-2.5	v
Gate-Source Leakage	I _{GSS}	V _{DS} =	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	± 100	nA
		$V_{GS} = 0 V$	V _{DS} = - 100 V	-	-	- 1	μА
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 V$	V _{DS} = - 100 V, T _J = 125 °C	-	-	- 50	
		$V_{GS} = 0 V$	$V_{DS} = -100 \text{ V}, T_{J} = 175 ^{\circ}\text{C}$	-	-	- 250	
On-State Drain Current ^a	I _{D(on)}	$V_{GS} = -10 \text{ V}$	$V_{DS} \le -5 V$	- 30	-	-	Α
		V _{GS} = - 10 V	I _D = - 9.2 A	-	0.033	-	- Ω
Drain Course On State Besistance	R _{DS(on)}	$V_{GS} = -10 \text{ V}$	I _D = - 9.2 A, T _J = 125 °C	-	0.065	-	
Drain-Source On-State Resistance ^a	¹ DS(on)	$V_{GS} = -10 \text{ V}$	I _D = - 9.2 A, T _J = 175 °C	-	0.081	-	
		$V_{GS} = -4.5 \text{ V}$	I _D = - 7.7 A	-	0.037	-	
Forward Transconductanceb	9 _{fs}	V _{DS} = - 15 V, I _D = - 9.2 A		-	35	-	S
Dynamic ^b							
Input Capacitance	C _{iss}			-	4433	-	
Output Capacitance	C _{oss}	$V_{GS} = 0 V$	$V_{DS} = -25 \text{ V}, f = 1 \text{ MHz}$	-	301	-	pF
Reverse Transfer Capacitance	C _{rss}			-	208	-	
Total Gate Charge ^c	Q_{g}			-	96	144	
Gate-Source Charge ^c	Q_{gs}	$V_{GS} = -10 \text{ V}$	$V_{DS} = -50V$, $I_{D} = -9.2$ A	-	8.4	-	nC
Gate-Drain Charge ^c	Q_{gd}			-	23.5	-	
Gate Resistance	R _g	f = 1 MHz		1.5	3.13	4.7	Ω
Turn-On Delay Time ^c	t _{d(on)}				11	17	
Rise Time ^c	t _r	$V_{DD} = -50 \text{ V}, R_L = 6.49 \Omega$ $I_D \cong -7.7 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 1.0 \Omega$		-	11	17	ns
Turn-Off Delay Time ^c	t _{d(off)}			-	78	117	
Fall Time ^c	t _f			-	15	23	
Source-Drain Diode Ratings and Chara	acteristics ^b				•		
Pulsed Current ^a	I _{SM}			-	-	- 150	Α
Forward Voltage	V_{SD}	I _F = - 7.7 A, V _{GS} = 0 V		_	- 0.8	- 1.5	V

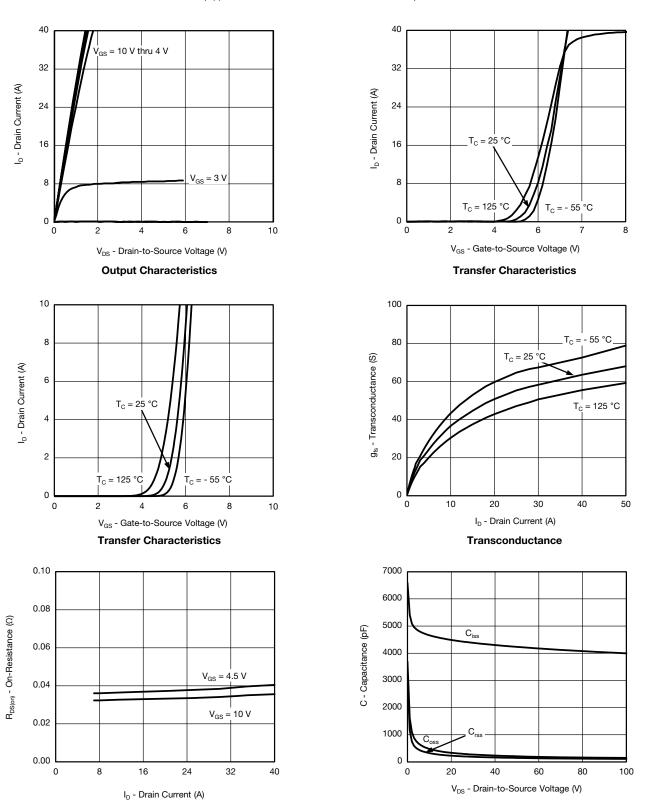
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)

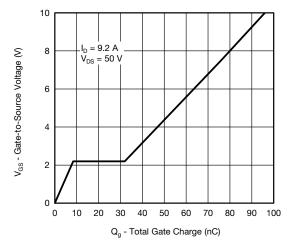


On-Resistance vs. Drain Current

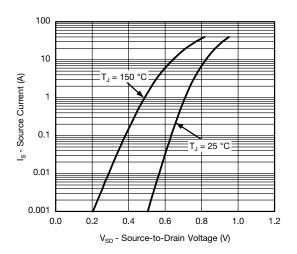
Capacitance



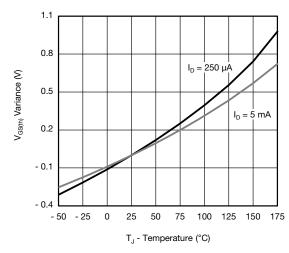
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



Gate Charge

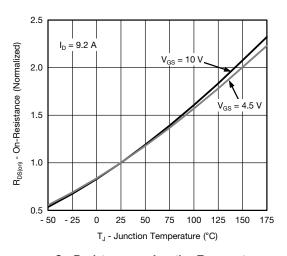


Source Drain Diode Forward Voltage

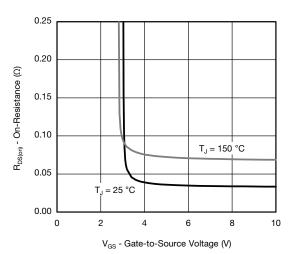


Threshold Voltage

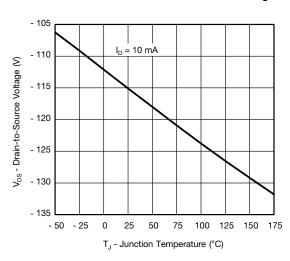
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On-Resistance vs. Junction Temperature



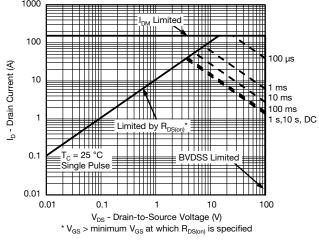
On-Resistance vs. Gate-to-Source Voltage



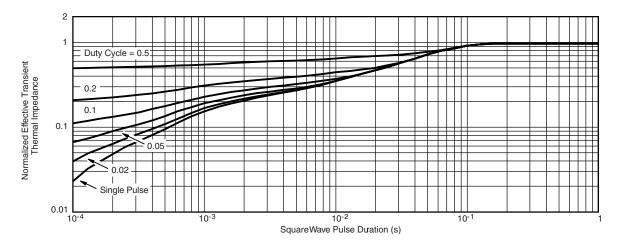
Drain Source Breakdown vs. Junction Temperature



THERMAL RATINGS ($T_A = 25$ °C, unless otherwise noted)



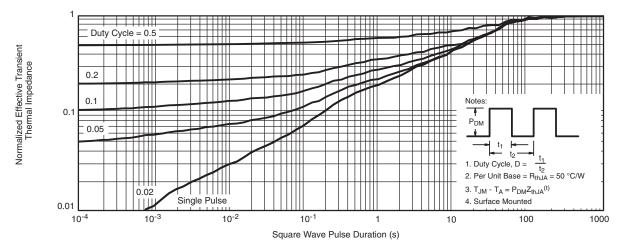
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



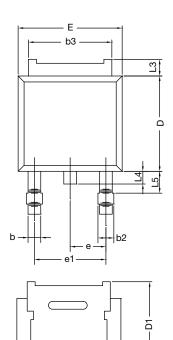
Normalized Thermal Transient Impedance, Junction-to-Ambient

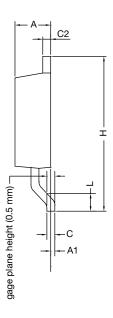
Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction to Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction to Case (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.



TO-252AA Case Outline





	MILLIN	METERS	RS INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
Α	2.18	2.38	0.086	0.094	
A1	-	0.127	-	0.005	
b	0.64	0.88	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	
С	0.46	0.61	0.018	0.024	
C2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	
D1	4.10	-	0.161	-	
Е	6.35	6.73	0.250	0.265	
E1	4.32	-	0.170	-	
Н	9.40	10.41	0.370	0.410	
е	2.28	BSC	0.090	BSC	
e1	4.56 BSC		0.180 BSC		
L	1.40	1.78	0.055	0.070	
L3	0.89	1.27	0.035	0.050	
L4	-	1.02	-	0.040	
L5	1.01	1.52	0.040	0.060	
ECN: T13-0592-Rev. A, 02-Sep-13					

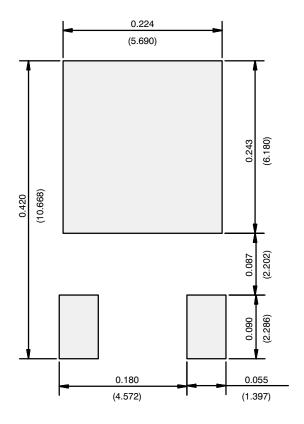
DWG: 6019

Note

• Dimension L3 is for reference only.



RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads Dimensions in Inches/(mm)



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