

## NCE P-Channel Enhancement Mode Power MOSFET

### Description

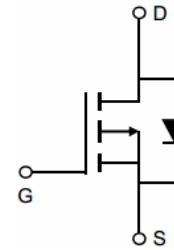
The NCE2305 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications.

### General Features

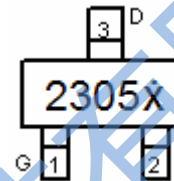
- $V_{DS} = -20V, I_D = -4.1A$   
 $R_{DS(ON)} < 45m\Omega @ V_{GS} = -4.5V$   
 $R_{DS(ON)} < 60m\Omega @ V_{GS} = -2.5V$   
 $R_{DS(ON)} < 90m\Omega @ V_{GS} = -1.8V$
- High power and current handling capability
- Surface mount package
- Pb free terminal plating
- RoHS compliant
- Halogen free

### Application

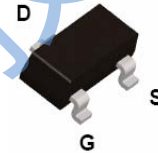
- PWM applications
- Load switch
- Power management



Schematic diagram



Marking and pin assignment



SOT-23 top view

### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
2305 X	NCE2305	SOT-23	Ø180mm	8 mm	3000 units

### Absolute Maximum Ratings ( $T_A = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	-20	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	V
Continuous Drain Current	$I_D$	$T_C = 25^\circ C$	-5.4
		$T_C = 70^\circ C$	-4.3
		$T_A = 25^\circ C$	-4.1
		$T_A = 70^\circ C$	-3.2
Drain Current - Pulsed (Note 1)	$I_{DM}$	-20	A
Maximum Power Dissipation	$P_D$	$T_C = 25^\circ C$	1.7
		$T_C = 70^\circ C$	1.1
		$T_A = 25^\circ C$	1.0
		$T_A = 70^\circ C$	0.65
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	$^\circ C$

### Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	125	$^\circ C/W$
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**Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)**

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-20	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-20V, V <sub>GS</sub> =0V	-	-	-1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±12V, V <sub>DS</sub> =0V	-	-	±100	nA
<b>On Characteristics</b> (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-0.45	-0.7	-1.0	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-4.1A	-	34	45	mΩ
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-3A	-	44	60	
		V <sub>GS</sub> =-1.8V, I <sub>D</sub> =-2A	-	60	90	
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =-5V, I <sub>D</sub> =-4.1A	-	6	-	S
<b>Dynamic Characteristics</b> (Note4)						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-10V, V <sub>GS</sub> =0V, F=1.0MHz	-	1120	-	PF
Output Capacitance	C <sub>oss</sub>		-	105	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	100	-	PF
<b>Switching Characteristics</b> (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =-10V, R <sub>L</sub> =-1.2Ω, V <sub>GEN</sub> =-4.5V, R <sub>g</sub> =1Ω	-	12	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	35	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	30	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	10	-	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-10V, I <sub>D</sub> =-4.1A, V <sub>GS</sub> =-4.5V	-	13.1	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	2.0	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	2.7	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =-4.1A	-	-	-1.2	V
Diode Forward Current (Note 2)	I <sub>S</sub>		-	-	-4.1	A

**Notes:**

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production

Typical Electrical and Thermal Characteristics

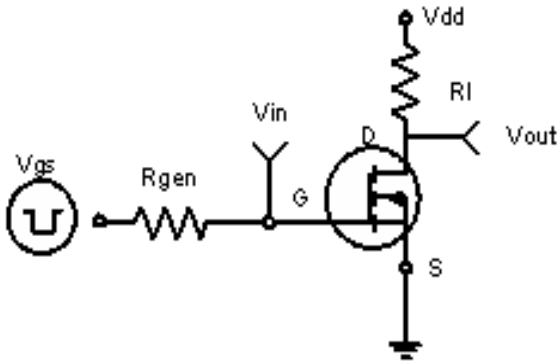


Figure 1: Switching Test Circuit

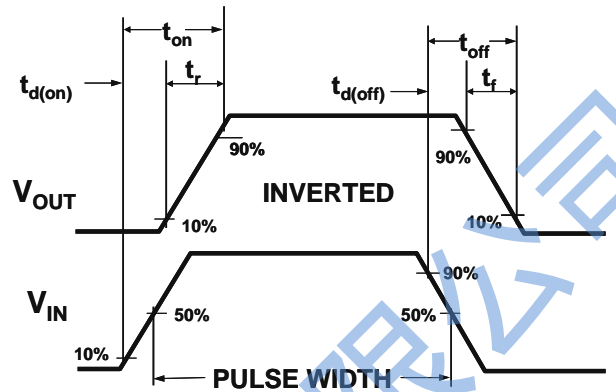


Figure 2: Switching Waveforms

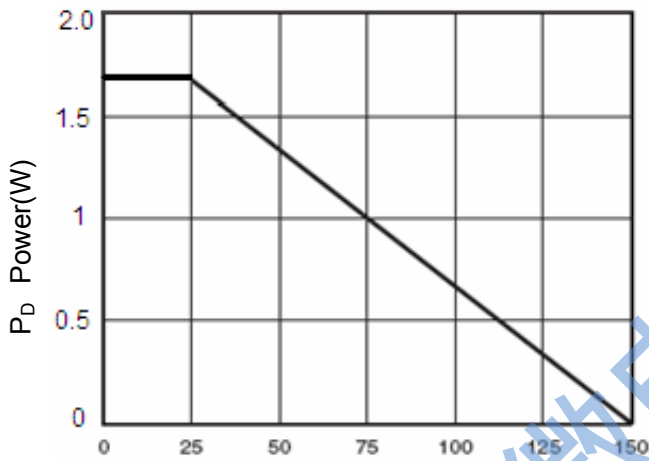


Figure 3 Power Dissipation

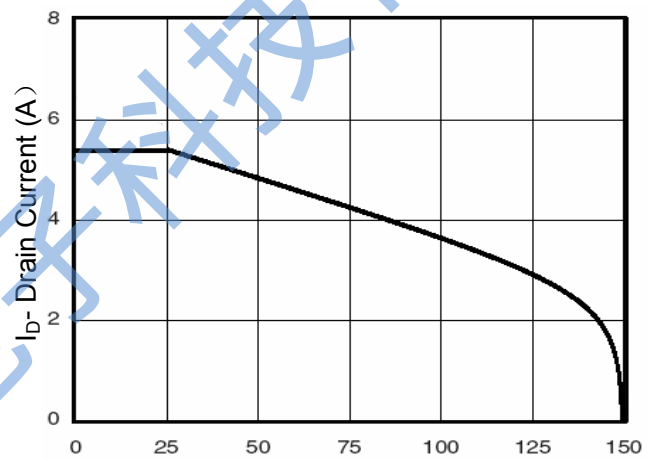


Figure 4 Drain Current

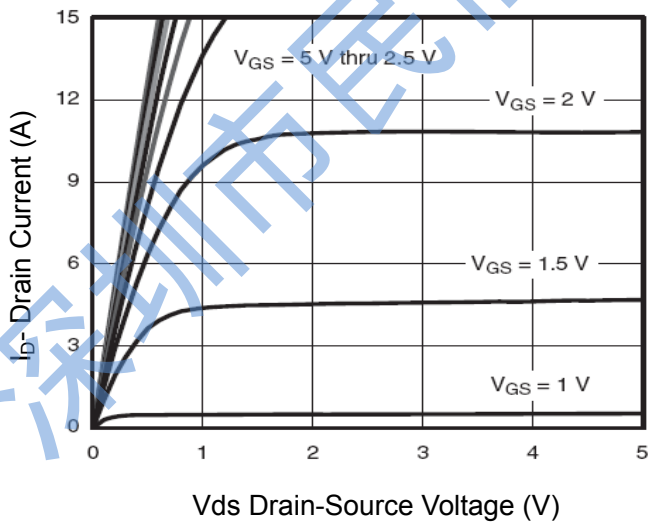


Figure 5 Output Characteristics

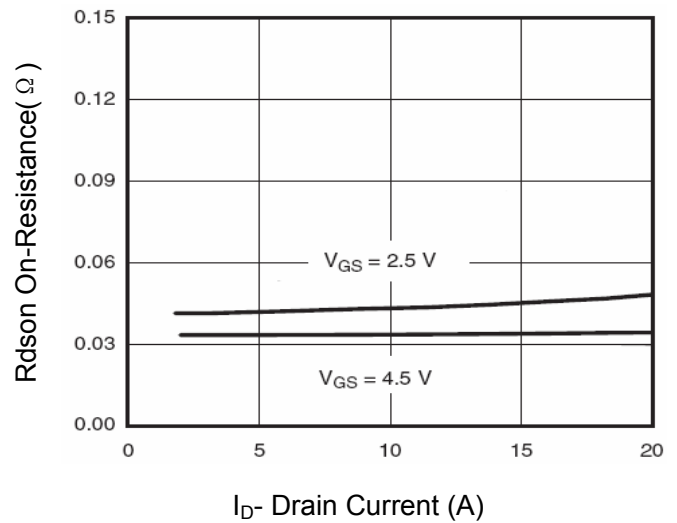
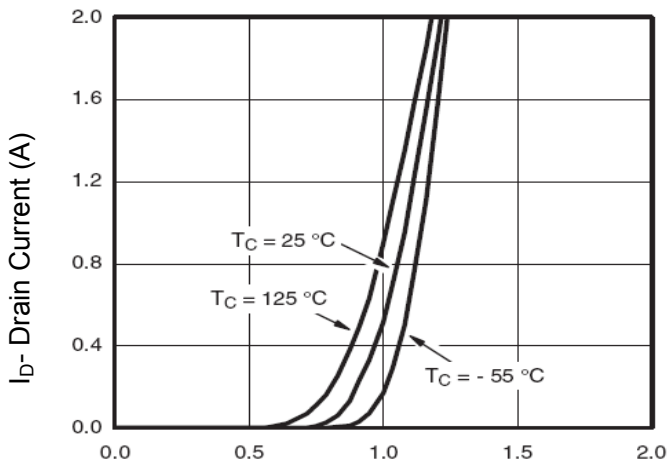
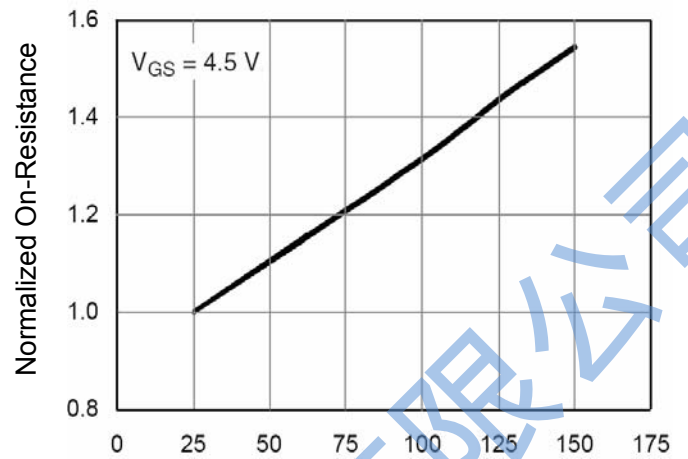


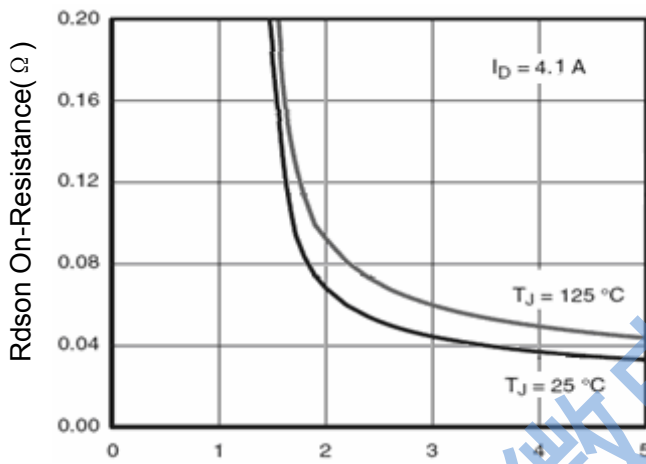
Figure 6 Drain-Source On-Resistance



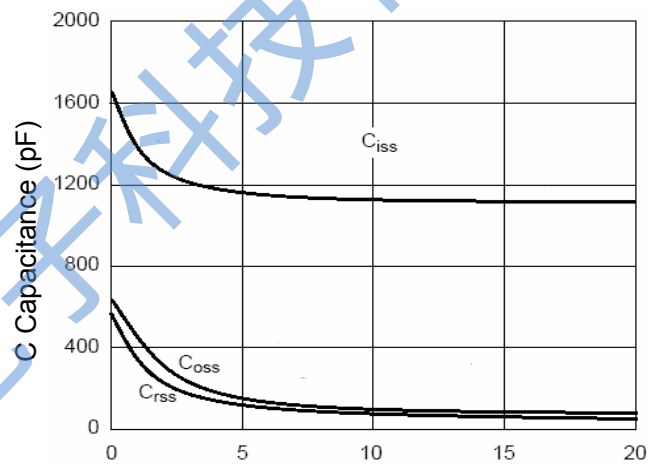
Vgs Gate-Source Voltage (V)  
**Figure 7 Transfer Characteristics**



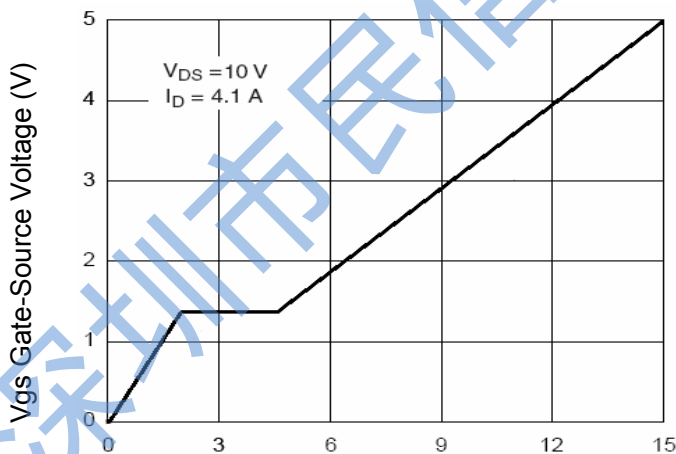
$T_J$ -Junction Temperature( $^\circ\text{C}$ )  
**Figure 8 Drain-Source On-Resistance**



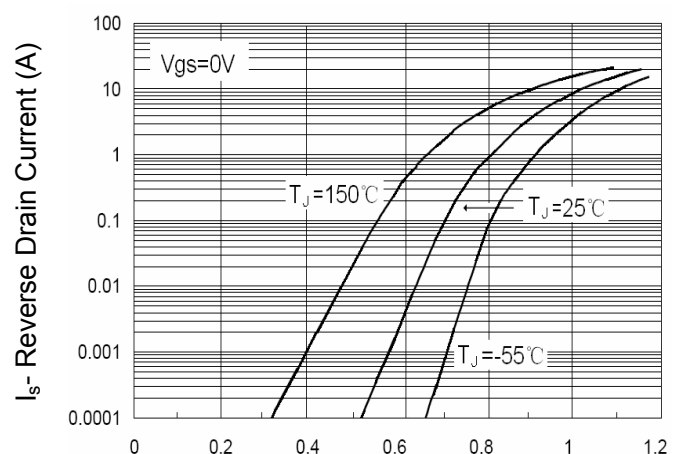
Vgs Gate-Source Voltage (V)  
**Figure 9 Rdson vs Vgs**



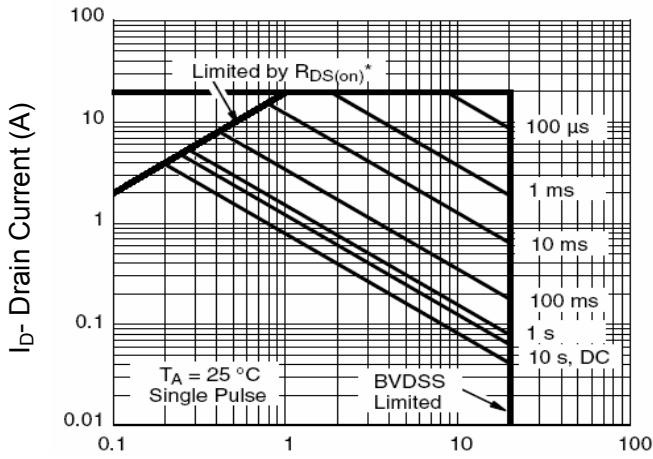
Vds Drain-Source Voltage (V)  
**Figure 10 Capacitance vs Vds**



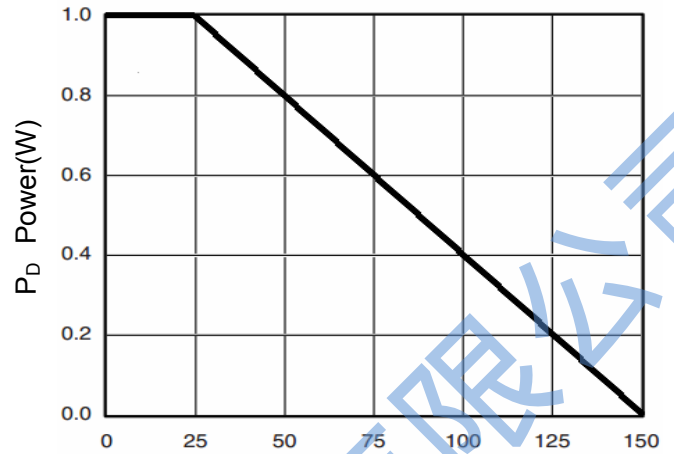
Qg Gate Charge (nC)  
**Figure 11 Gate Charge**



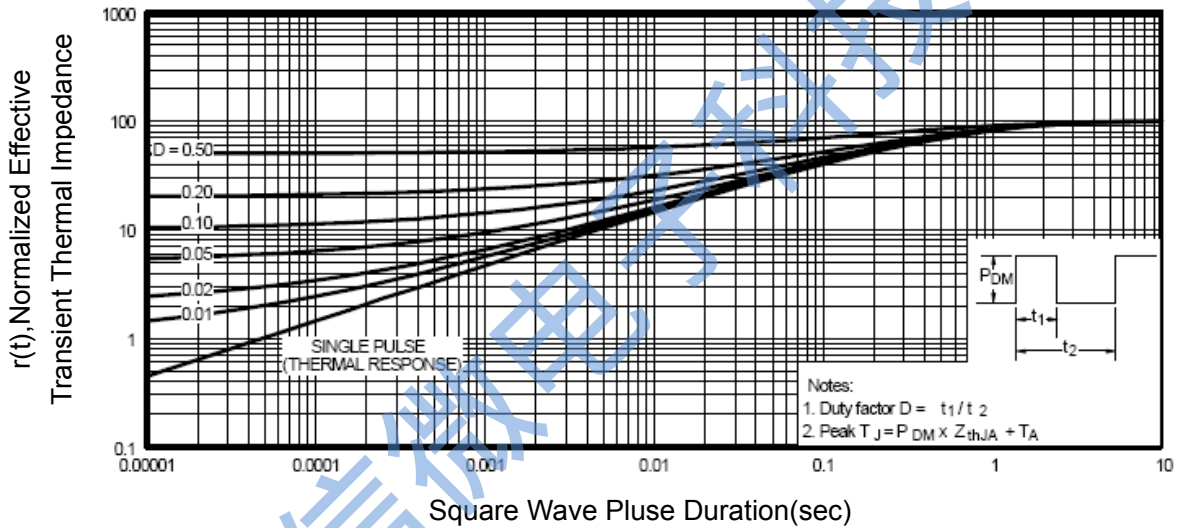
Vsd Source-Drain Voltage (V)  
**Figure 12 Source- Drain Diode Forward**



Vds Drain-Source Voltage (V)  
**Figure 13 Safe Operation Area**

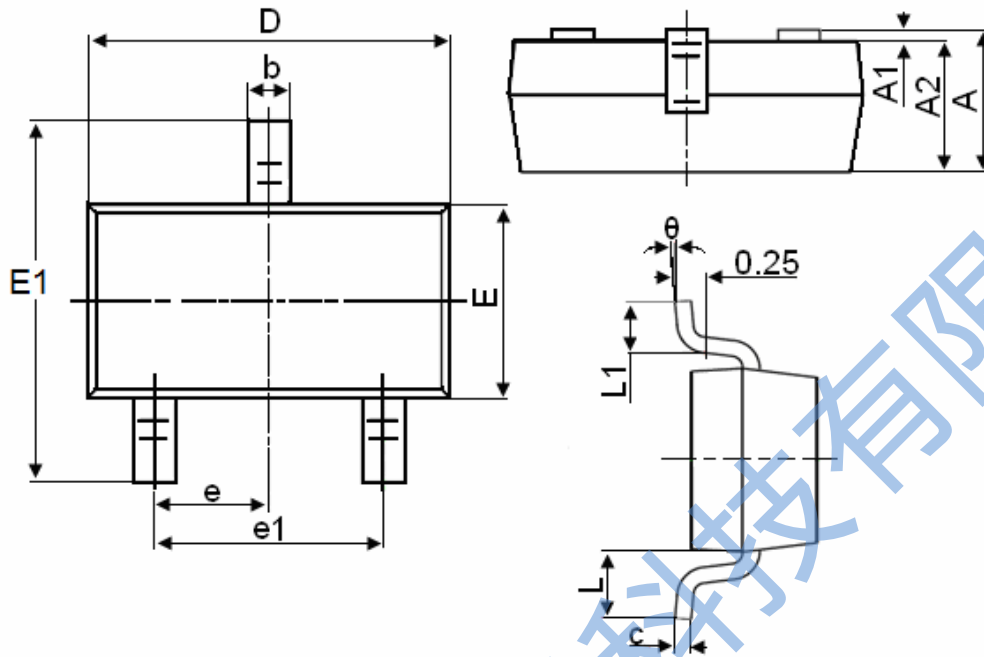


$T_A$ -Ambient Temperature ( $^\circ\text{C}$ )  
**Figure 14 Power Dissipation**



Square Wave Pulse Duration(sec)  
**Figure 15 Normalized Maximum Transient Thermal Impedance**

SOT-23 Package Information

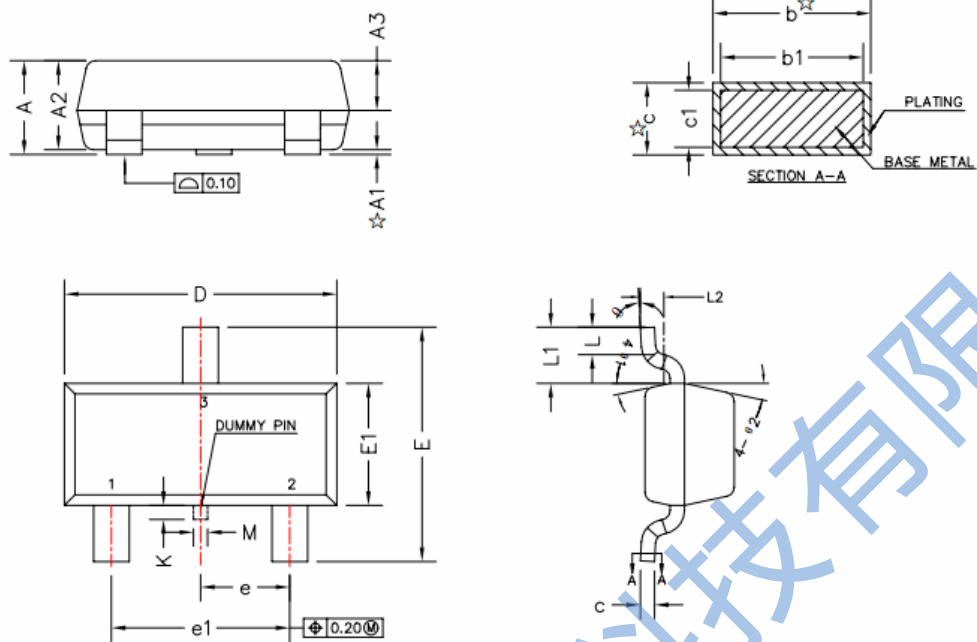


Symbol	Dimensions in Millimeters	
	MIN.	MAX.
A	0.900	1.150
A1	0.000	0.100
A2	0.900	1.050
b	0.300	0.500
c	0.080	0.150
D	2.800	3.000
E	1.200	1.400
E1	2.250	2.550
e	0.950TYP	
e1	1.800	2.000
L	0.550REF	
L1	0.300	0.500
$\theta$	0°	8°

Notes

1. All dimensions are in millimeters.
2. Tolerance  $\pm 0.10\text{mm}$  (4 mil) unless otherwise specified
3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
4. Dimension L is measured in gauge plane.
5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

SOT-23 Package Information



Symbol	Millimeters	
	Min.	Max.
A	0.89	1.12
A1	0.01	0.10
A2	0.88	1.02
A3	0.43	0.63
b	0.36	0.50
b1	0.35	0.45
c	0.14	0.20
c1	0.14	0.16
D	2.80	3.00
E	2.35	2.64
E1	1.20	1.40
e	0.90	1.00
e1	1.80	2.00
L	0.40	0.60
L1	0.6REF	
L2	0.25BSC	
M	0.10	0.25
K	0.00	0.25
θ	0°	8°
θ1	10°	14°
θ2	10°	14°

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