

Features

- ESD Protected up to 2KV (HBM)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

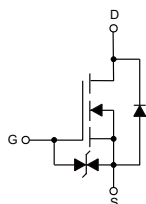
Maximum Ratings

- Operating Junction Temperature Range: -55°C to +150°C
- Storage Temperature: -55°C to +150°C
- Thermal Resistance: 150°C/W Junction to Ambient

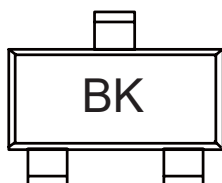
Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	50	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	0.59	A
Power Dissipation	P_D	0.83	W

Note: 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

Internal Structure and Marking Code

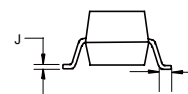
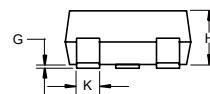
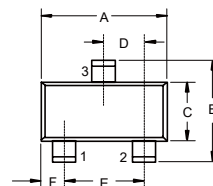


1. GATE
2. SOURCE
3. DRAIN



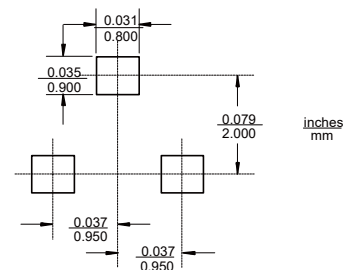
N-Channel MOSFET

SOT-23



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.110	0.120	2.80	3.04	
B	0.083	0.104	2.10	2.64	
C	0.047	0.055	1.20	1.40	
D	0.034	0.041	0.85	1.05	
E	0.067	0.083	1.70	2.10	
F	0.018	0.024	0.45	0.60	
G	0.0004	0.006	0.01	0.15	
H	0.035	0.043	0.90	1.10	
J	0.003	0.007	0.08	0.18	
K	0.012	0.020	0.30	0.51	
L	0.007	0.020	0.20	0.50	

Suggested Solder Pad Layout



Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	50			V
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±10	μA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =50V, V _{GS} =0V			1	μA
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	0.5		1.5	V
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =10V, I _D =0.5A		1.06	1.5	Ω
		V _{GS} =4.5V, I _D =0.2A		1.23	2.3	
		V _{GS} =2.5V, I _D =0.1A		2.64	4.1	
Diode Characteristics						
Continuous Body Diode Current	I _S				0.5	A
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =0.5A			1.3	V
Reverse Recovery Time	t _{rr}	I _S =0.5A, di/dt=100A/μs		9.3		ns
Reverse Recovery Charge	Q _{rr}			4.1		nC
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =30V, V _{GS} =0V, f=1MHz		58		pF
Output Capacitance	C _{oss}			16		
Reverse Transfer Capacitance	C _{rss}			9.4		
Total Gate Charge	Q _g	V _{DS} =30V, V _{GS} =10V, I _D =0.5A		1.4		nC
Gate-Source Charge	Q _{gs}			0.3		
Gate-Drain Charge	Q _{gd}			0.14		
Turn-On Delay Time	t _{d(on)}	V _{DS} =30V, V _{GEN} =10V, R _G =3.9Ω, R _L =60Ω, I _{DS} =0.5A		2.6		ns
Turn-On Rise Time	t _r			2.2		
Turn-Off Delay Time	t _{d(off)}			7.3		
Turn-Off Fall Time	t _f			6.2		

Curve Characteristics

Fig. 1 - Typical Output Characteristics

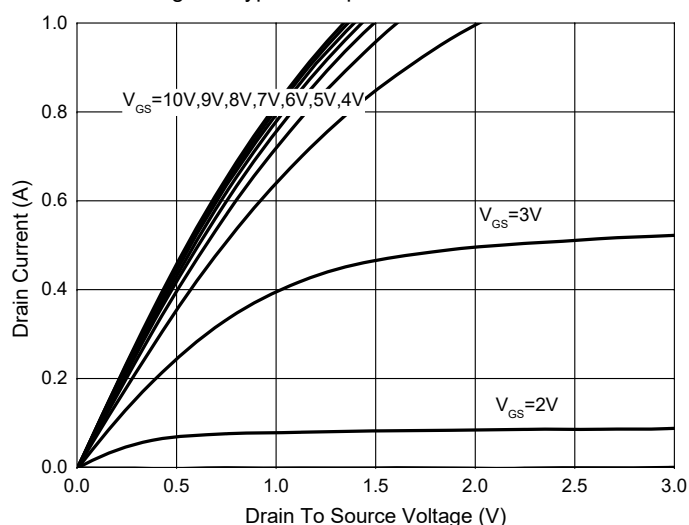


Fig. 2 - $R_{DS(ON)} - I_D$

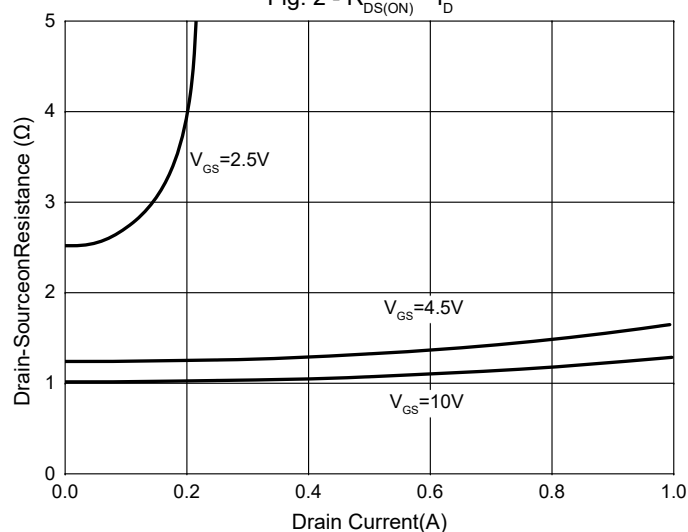


Fig. 3 - Normalized Threshold Voltage

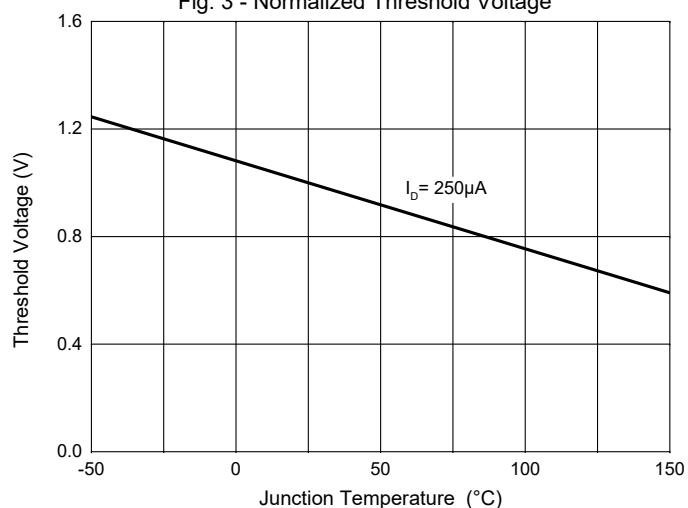


Fig. 4 - $I_S - V_{SD}$

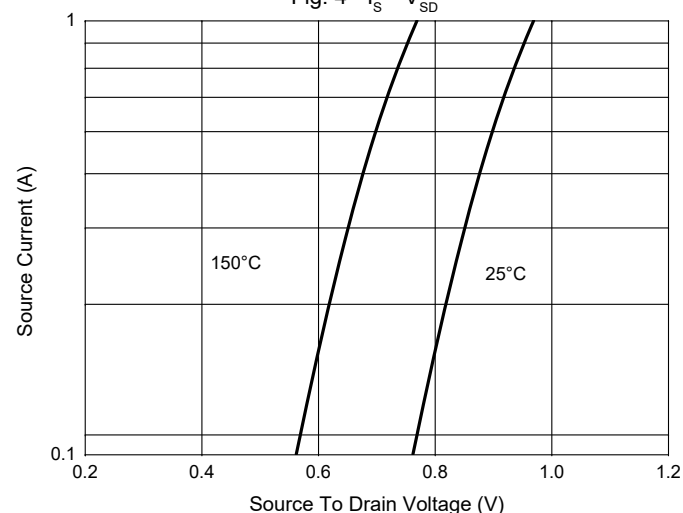


Fig. 5 - Normalized On Resistance Characteristics

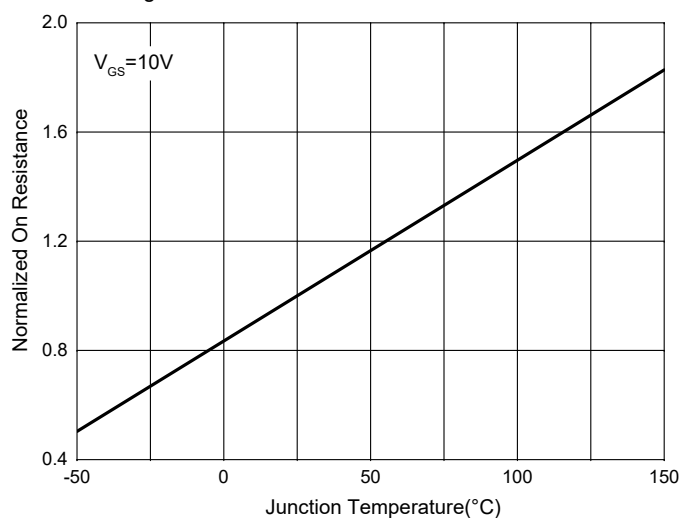
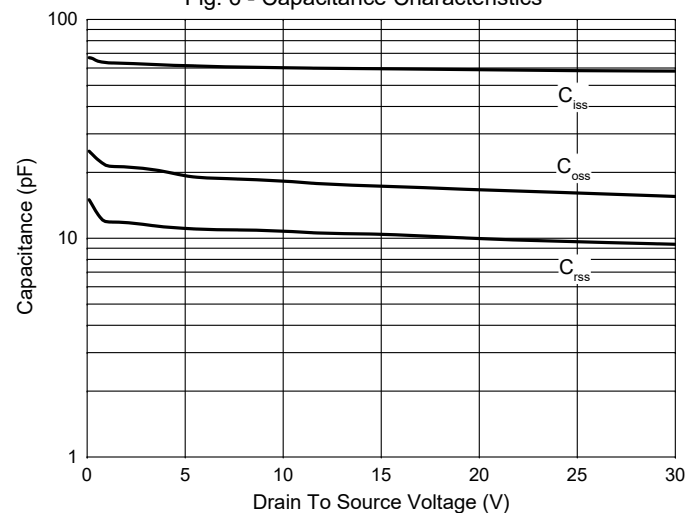


Fig. 6 - Capacitance Characteristics



Curve Characteristics

Fig. 7 - Gate Charge

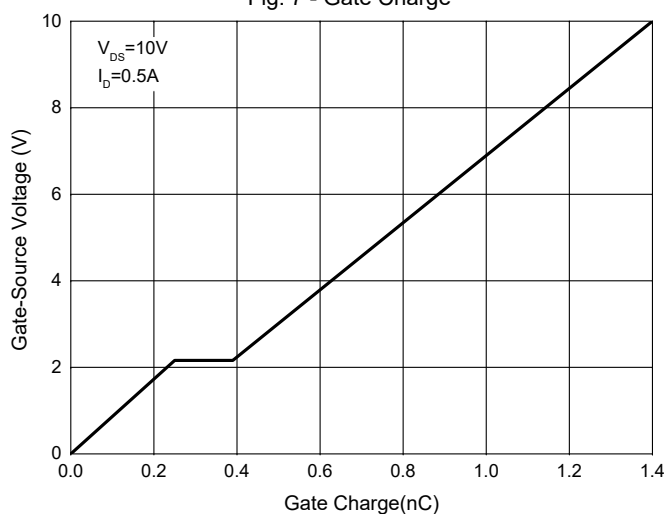


Fig. 8 - Safe Operation Area

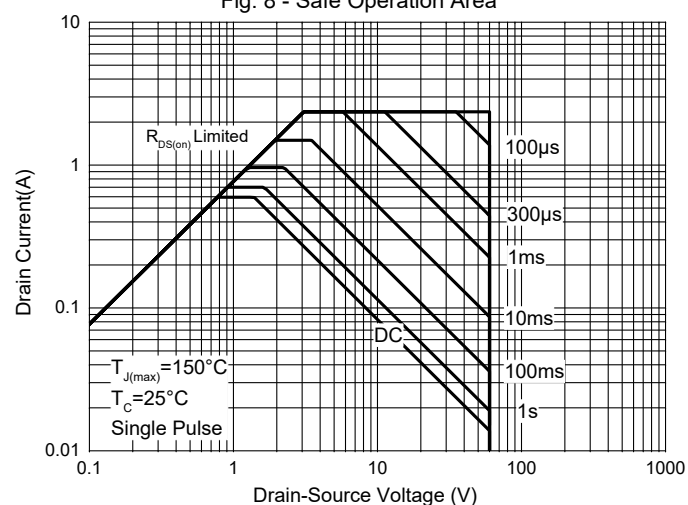
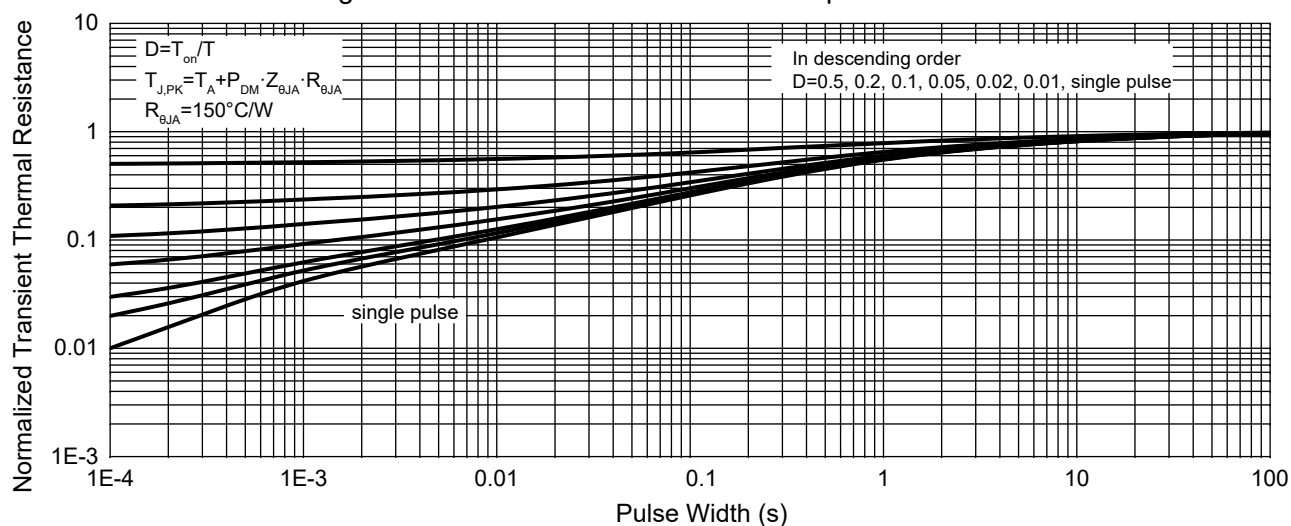


Fig. 9 - Normalized Transient Thermal Impedance



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel:3Kpcs/Reel

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