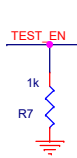


所有电容靠近芯片pin脚

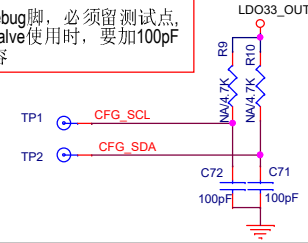
注意事项:
Billboard网络可根据客户具体要求二选一
放置于ALT Port (J1) 或PD Port (USB1),
取决于是否应用USB2.0或做billboard认
证,若无需求,优先放置ALT Port



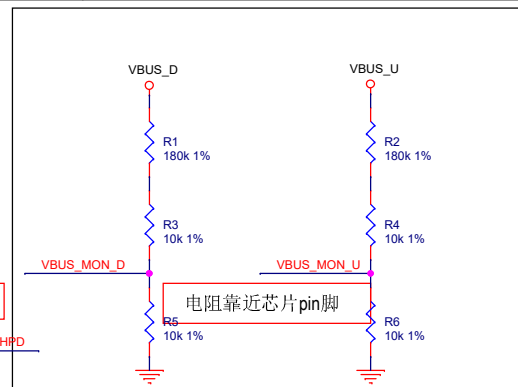
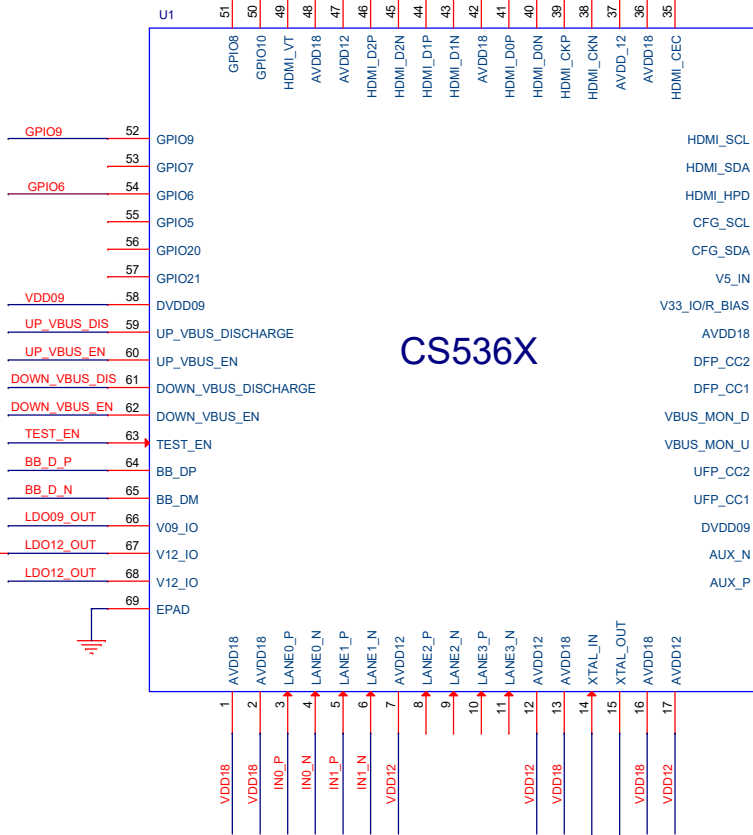
- 4 HDMI_CEC
- 4 HDMI_HPD
- 4 HDMI_SDA
- 4 HDMI_SCL
- 4 HDMI_CK_N
- 4 HDMI_CK_P
- 4 HDMI_D0_N
- 4 HDMI_D0_P
- 4 HDMI_D1_N
- 4 HDMI_D1_P
- 4 HDMI_D2_N
- 4 HDMI_D2_P

- HDMI_CEC
- HDMI_HPD
- HDMI_SDA
- HDMI_SCL
- HDMI_CK_N
- HDMI_CK_P
- HDMI_D0_N
- HDMI_D0_P
- HDMI_D1_N
- HDMI_D1_P
- HDMI_D2_N
- HDMI_D2_P

CFG为debug脚, 必须留测试点,
且作为salve使用时, 要加100pF
滤波电容



CS536X



1. 对于5365/5366, 5V_IN预留20hm, 用于改善EOS环境
2. 对于5363, pin29为chip 3.3V供电引脚, pin29 floating

HDMI_HPD预留1kohm
用于改善EOS环境

DPF_CC1/DFP_CC2及UFP_CC1/UFP_CC2
未使用时, 建议下拉到GND

3 DOWN_VBUS_EN >>> DOWN_VBUS_EN
3 DOWN_VBUS_DIS >>> DOWN_VBUS_DIS

3 UP_VBUS_EN >>> UP_VBUS_EN
3 UP_VBUS_DIS >>> UP_VBUS_DIS

4 UFP_CC1 >>> UFP_CC1

4 UFP_AUX_N >>> UFP_AUX_N
4 UFP_AUX_P >>> UFP_AUX_P

4 IN0_N >>> IN0_N
4 IN0_P >>> IN0_P

4 IN1_N >>> IN1_N
4 IN1_P >>> IN1_P

4 GPIO9 >>> GPIO9
4 GPIO8 >>> GPIO8
4 GPIO6 >>> GPIO6

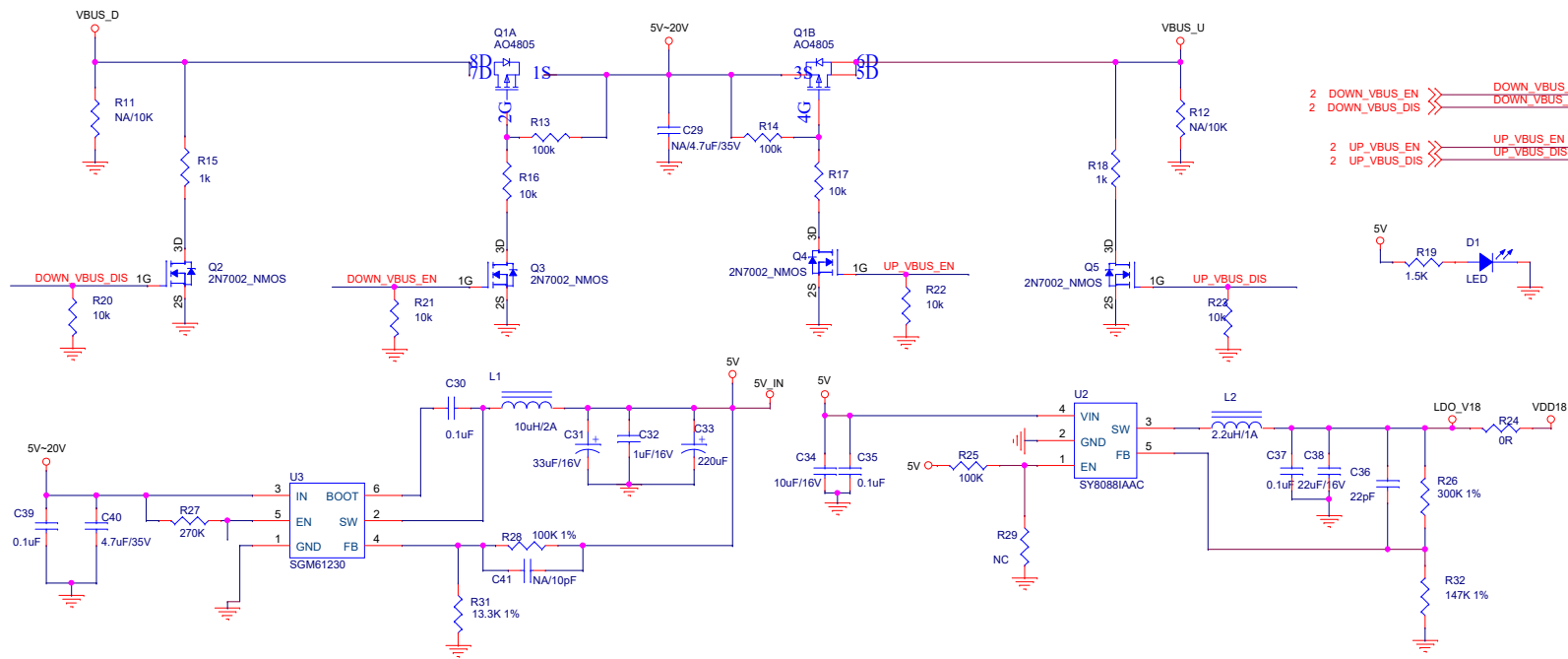
4 BB_D_N >>> BB_D_N
4 BB_D_P >>> BB_D_P

4 DFP_CC1 >>> DFP_CC1
4 DFP_CC2 >>> DFP_CC2

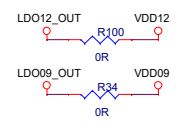
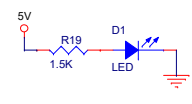
使用芯片内部晶振时, X1/R8/C27/C28可不贴,但
须保留位置用于Debug



Title		CS536X	
Size	Document Number	Rev	
Custom	P02 Main chip	V1.1	
Date:	Tuesday, July 04, 2023	Sheet	2 of 4

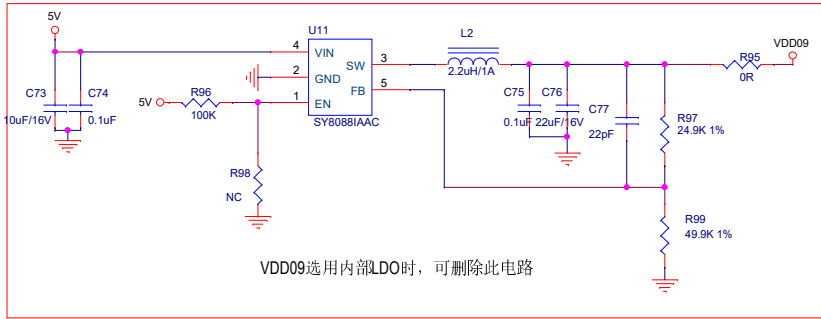


2 DOWN_VBUS_EN <-> DOWN_VBUS_EN
 2 DOWN_VBUS_DIS <-> DOWN_VBUS_DIS
 2 UP_VBUS_EN <-> UP_VBUS_EN
 2 UP_VBUS_DIS <-> UP_VBUS_DIS



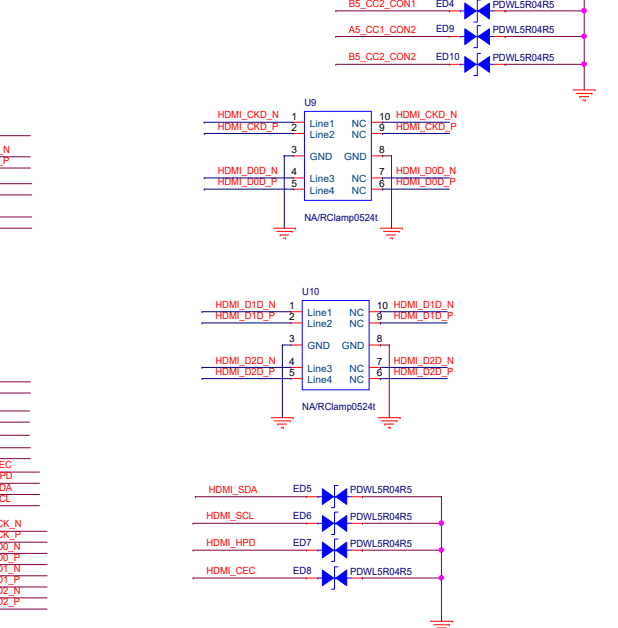
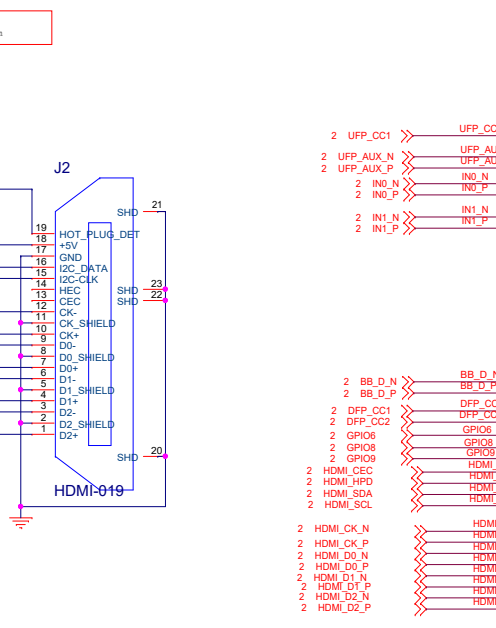
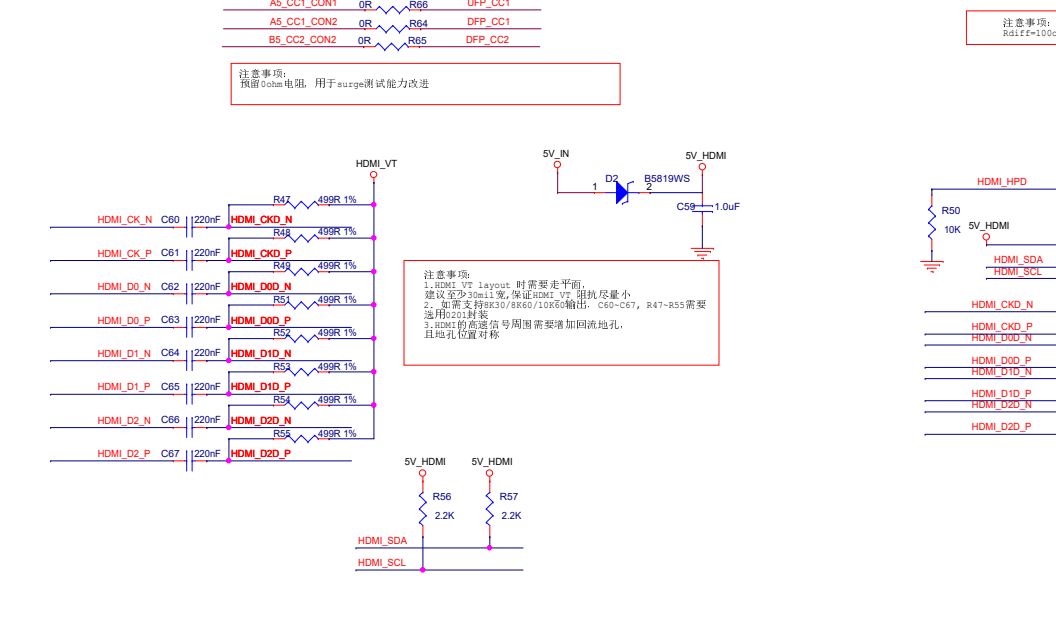
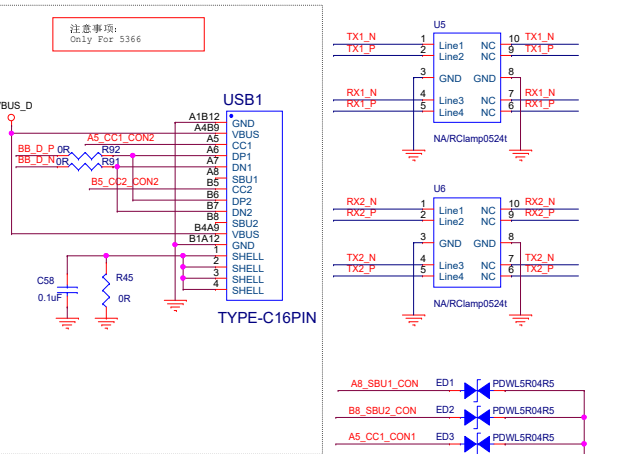
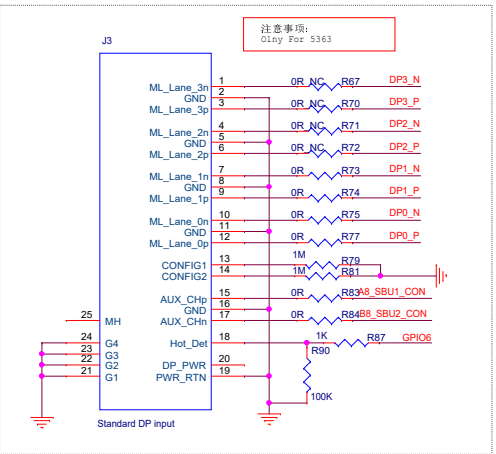
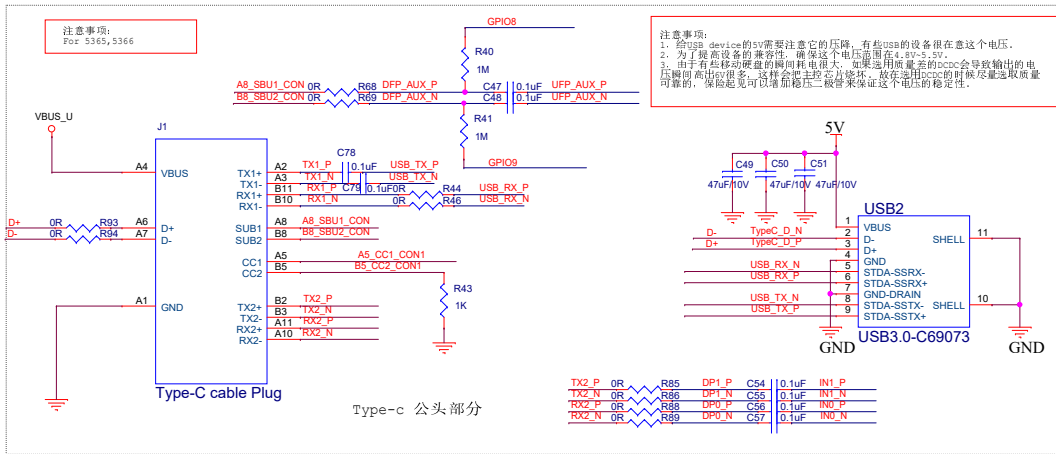
1.VDD18必须用外部供电。
 2.VDD09可以选择内部LDO或外部DC供电，外部供电功耗更好，外部供电时，须将R34断开

注意事项：
 本页电路应用于5366，对于5363/5365，客户可根据需求对供电外围电路做相应增减



VDD09选用内部LDO时，可删除此电路

Title		
CS536X		
Size	Document Number	Rev
CustomP03 Power		V1.1
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Title		CS536X	Rev	V1.1
Size	Document Number			
Custom	P04 Connector			
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