

# DO-214AC(SMA) @10/700µS, 2KV Thyristor Surge Suppressors (TSS)

# **Description**

P0080TA - P5000TA Series are designed to protect broadband equipment such as modems, line card, CPE and DSL from damaging over-voltage transients.

The series provides a surface mount solution that enables equipment to comply with global regulatory standards.

## **Features and Benefits**

- ◆ Low voltage overshoot
- ◆ Low on-state voltage
- Does not degrade surge capability after multiple surge events within limit
- ◆ Fails short circuit when surged in excess of ratings
- ◆ Low Capacitance

# **Applicable Global Standards**

- ◆ TIA-968-A
- ◆ ITU K.20/21 Enhanced level
- ◆ ITU K.20/21 Basic Level
- GR 1089 Inter building
- ♦ IEC 6100-4-5
- ♦ YD/T 1082
- ♦ YD/T 993
- ♦ YD/T 950



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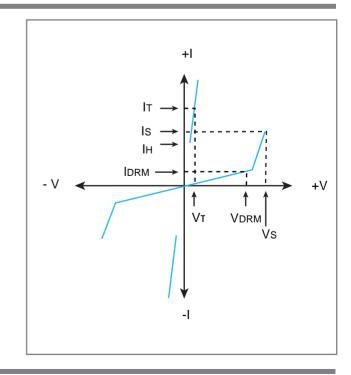


# **Schematic Symbol**



#### **Electrical Parameters**

Parameter	Definition
Is	Switching Current - maximum current required to switch to on state
IDRM	Leakage Current - maximum peak off-state current measured at VDRM
Ін	Holding Current - minimum current required to maintain on state
lτ	On-state Current - maximum rated continuous on-state bcurrent
Vs	Switching Voltage - maximum voltage prior to switching to on stat
VDRM	Peak Off-state Voltage - maximum voltage that can be applied while maintaining off state
Vī	On-state Voltage - maximum voltage measured at rated on-state current
Co	Off-state Capacitance - typical capacitance measured in off state





Thyristor Surge Suppressors (TSS)
DO-214AC(SMA) @10/700µS, 2KV ROHS

## **Electrical Characteristics**

Part Number	Marking	Vdrm @Idrm=5µA	IDRM	Vs @100V/μS	ls	VT @IT=2.2A	lτ	Ін	Co @1MHz
T dit (dillio)	Marking	VMin.	μΑMax.	VMax.	mAMax.	VMax.	AMax.	mAMin.	pFMax.
P0080TA	P008A	6	5	25	800	4	2.2	50	50
P0300TA	P03A	25	5	40	800	4	2.2	50	70
P0640TA	P06A	58	5	77	800	4	2.2	150	50
P0720TA	P07A	65	5	88	800	4	2.2	150	50
P0900TA	P09A	75	5	98	800	4	2.2	150	45
P1100TA	P11A	90	5	130	800	4	2.2	150	45
P1300TA	P13A	120	5	160	800	4	2.2	150	45
P1500TA	P15A	140	5	180	800	4	2.2	150	40
P1800TA	P18A	170	5	220	800	4	2.2	150	40
P2000TA	P20A	180	5	220	800	4	2.2	150	40
P2300TA	P23A	190	5	260	800	4	2.2	150	35
P2600TA	P26A	220	5	300	800	4	2.2	150	35
P3100TA	P31A	275	5	350	800	4	2.2	150	30
P3500TA	P35A	320	5	400	800	4	2.2	150	30
P3800TA	P38A	360	5	460	800	4	2.2	150	30
P4200TA	P42A	400	5	520	800	4	2.2	150	30
P4500TA	P45A	420	5	540	800	4	2.2	150	30
P5000TA	P50A	440	5	600	800	4	2.2	150	30

#### Notes:

- Absolute maximum ratings measured at TA= 25°C (unless otherwise noted).
- Devices are bi-directional.

# **Surge Ratings**

	2/10µS¹	8/20µS¹	10/560µS¹	10/560µS¹	10/1000µS¹	5/320µS¹	Ітѕм	di/dt
Series	2/10µS²	1.2/50µS²	10/560µS²	10/560µS²	10/1000µS²	10/700µS²	50/60Hz	ui/ut
	A min	A min	A min	A min	A min	A min	A min	Amps/µs max
А	150	150	90	50	45	50	20	500

#### Notes:

- 1. Current waveform in µs
- Peak pulse current rating (IPP) is repetitive and guaranteed for the life of the product.
- 2. Voltage waveform in μs
- IPP ratings applicable over temperature range of -40°C to +85°C

- The device must initially be in thermal equilibrium with  $-40^{\circ}$ C < TJ <  $+150^{\circ}$ C

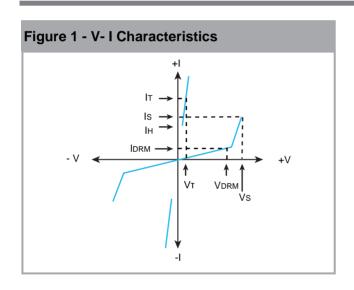


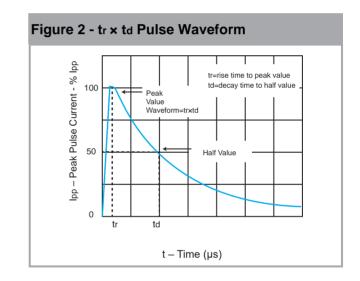
Thyristor Surge Suppressors (TSS)
DO-214AC(SMA) @10/700µS, 2KV ROHS

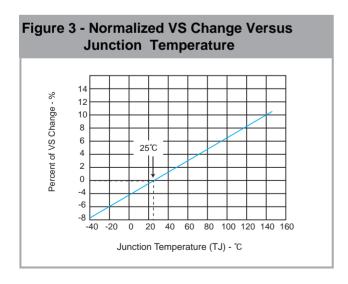
#### **Thermal Considerations**

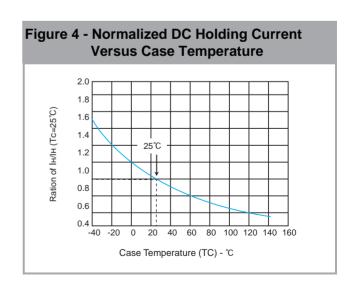
Package	Symbol	Parameter	Value	Unit
DO-214AC	TJ	Operating Junction Temperature Range	- 40 to +150	°C
	Ts	Storage Temperature Range	- 40 to +150	°C
	Rеja	Thermal Resistance: Junction to Ambient	90	°C/W

#### **Characteristic Curves**











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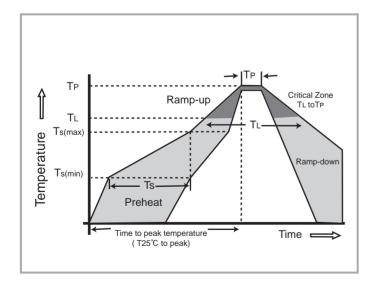
# **Environmental Specifications**

High Temp Voltage Blocking	80% Rated VDRM (VAC Peak ) +125°C or +150°C, Lead Material Copper Alloy High Temp Voltage Blocking 504 or 1008 hrs. MIL-STD-750 (Method 1040) JEDEC, JESD22-A-101		
Temp Cycing	-65°C to +150°C, 15 min. dwell, 10 up to 100 cycles.MIL-STD-750 (Method 1051) EIA/JEDEC, JESD22-A104		
Biased Temp & Humidity	52 VDC (+85°C) 85%RH, 504 up to 1008 hrs. EIA/ JEDEC, JESD22-A-101		
High Temp Storage	+150°C 1008 hrs. MIL-STD-750 (Method 1031) JEDEC, JESD22-A-101		
Low Temp Storage	-65°C, 1008 hrs.		
Thermal Shock	0°C to +100°C, 5 min. dwell, 10 sec. transfer, Thermal Shock 10 cycles. MIL-STD-750 (Method 1056) JEDEC, JESD22-A-106		
Autoclave (Pressure Cooker Test)	+121°C, 100%RH, 2atm, 24 up to 168 hrs. EIA/Cooker Test) JEDEC, JESD22-A-102		
Resistance to Solder Heat	+260°C, 30 secs. MIL-STD-750 (Method 2031		
Moisture Sensitivity Level	85%RH, +85°C, 168 hrs., 3 reflow cycles Level (+260°C Peak). JEDEC-J-STD-020, Level 1		

# **Physical Specifications**

Lead Material	Copper Alloy
Terminal Finish	100% Matte-Tin Plated
Body Material	UL recognized epoxy meeting flammability classification 94V-0

# **Soldering Parameters**



Reflow (	Condition	Lead-free assembly	
	-Temperature Min (Ts(min))	+150°C	
Pre Heat	-Temperature Max (Ts(max))	+200°C	
	- Time (min to max) (Ts)	60 -180 Seconds	
	ramp up rate ( Liquidus L) to peak	3°C/Second max	
Ts(max)	to TL - Ramp-up Rate	5°C/Second max	
D (1	- Temperature (TL) (Liquidus)	217°C	
Reflow	- Time (min to max) (Ts)	60 -150 Seconds	
Peak Te	mperature (TP)	260 +0/-5°C	
	thin 5°C of actual peak ature (TP)	30 Seconds Max	
Ramp-d	own Rate	6°C/Second Max	
Time 25°C to peak Temperature (TP)  Do not exceed		8 minutes Max	
		+260°C	

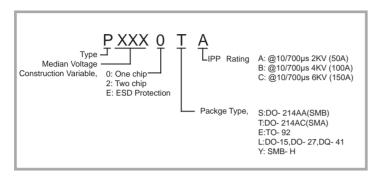


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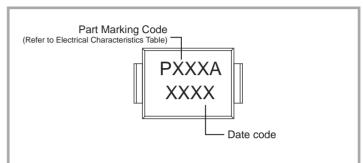
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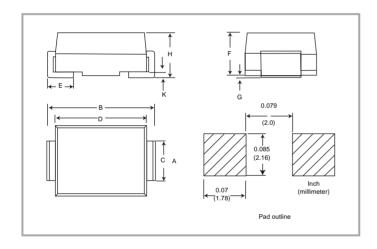
## **Part Numbering**



# **Part Marking**



#### **Dimensions DO-214AC**

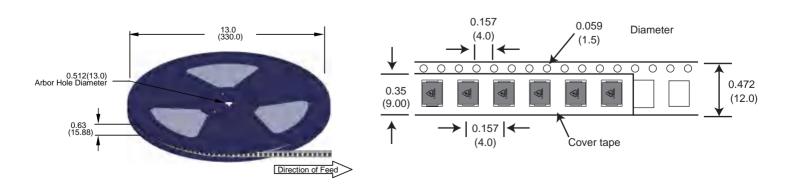


Dimensions	Inc	hes	Millimeters	
Dimensions	Min	Max	Min	Max
Α	0.100	0.110	2.54	2.79
В	0.194	0.208	4.93	5.28
С	0.049	0.065	1.25	1.65
D	0.157	0.177	3.99	4.50
E	0.030	0.060	0.76	1.52
F	0.076	0.096	1.90	2.45
G	0.002	0.008	0.05	0.20
Н	0.078	0.090	1.98	2.95
K	0.006	0.012	0.15	0.30

## **Packaging**

Part Number	Component Package	Quantity	Packaging Option	Packaging Specification
Pxxx0TA	DO-214AC	5000	Tape & Reel -12mm/13″tape	EIA -481

# **Tape and Reel Specifications**



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ROHS

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