ROHS

DO-214AA(SMB) @10/700µS, 6KV Thyristor Surge Suppressors (TSS)

Description

P0080SC- P5000SC Series are designed to protect broadband equipment such as modems, line card, CPE and DSL from damaging over-voltage transients.

The series provides a surface mount solution that enables equipment to comply with global regulatory standards.

Features and Benefits

- Low voltage overshoot
- Low on-state voltage
- Does not degrade surge capability after multiple surge events within limit
- Fails short circuit when surged in excess of ratings
- Low Capacitance

Applicable Global Standards

- ◆ TIA-968-A
- ITU K.20/21 Enhanced level
- ITU K.20/21 Basic Level
- GR 1089 Inter building
- IEC 6100-4-5
- YD/T 1082
- YD/T 993
- ♦ YD/T 950

Electrical Parameters

Parameter	Definition
ls	Switching Current - maximum current required to switch to on state
Idrm	Leakage Current - maximum peak off-state current measured at VDRM
Ін	Holding Current - minimum current required to maintain on state
Iτ	On-state Current - maximum rated continuous on-state bcurrent
Vs	Switching Voltage - maximum voltage prior to switching to on stat
Vdrm	Peak Off-state Voltage - maximum voltage that can be applied while maintaining off state
VT	On-state Voltage - maximum voltage measured at rated on-state current
Co	Off-state Capacitance - typical capacitance measured in off state



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Schematic Symbol





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Electrical Characteristics

Part Number	Marking	Vdrm @Idrm=5µA	Idrm	Vs @100V/µS	ls	Vт @IT=2.2А	lτ	Ін	Co @1MHz
	Marking	VMin.	µAMax.	VMax.	mAMax.	VMax.	AMax.	mAMin.	pFMax.
P0080SC	P008C	6	5	25	800	4	2.2	50	100
P0300SC	P03C	25	5	40	800	4	2.2	50	100
P0640SC	P06C	58	5	77	800	4	2.2	150	100
P0720SC	P07C	65	5	88	800	4	2.2	150	100
P0900SC	P09C	75	5	98	800	4	2.2	150	90
P1100SC	P11C	90	5	130	800	4	2.2	150	90
P1300SC	P13C	120	5	160	800	4	2.2	150	90
P1500SC	P15C	140	5	180	800	4	2.2	150	85
P1800SC	P18C	170	5	220	800	4	2.2	150	85
P2000SC	P20C	180	5	220	800	4	2.2	150	85
P2300SC	P23C	190	5	260	800	4	2.2	150	80
P2600SC	P26C	220	5	300	800	4	2.2	150	80
P3100SC	P31C	275	5	350	800	4	2.2	150	65
P3500SC	P35C	320	5	400	800	4	2.2	150	40
P3800SC	P38C	360	5	460	800	4	2.2	150	30
P4200SC	P42C	400	5	520	800	4	2.2	150	30
P4500SC	P45C	420	5	540	800	4	2.2	150	30
P5000SC	P50C	440	5	600	800	4	2.2	150	30

Notes:

- Absolute maximum ratings measured at TA= 25°C (unless otherwise noted).

- Devices are bi-directional.

Surge Ratings

Series	2/10µS¹	8/20µS¹	10/560µS¹	10/560µS¹	10/1000µS¹	5/320µS¹	Ітѕм	di/dt
	2/10µS²	1.2/50µS²	10/560µS²	10/560µS²	10/1000µS²	10/700µS²	50/60Hz	ui/ut
	A min	A min	A min	A min	A min	A min	A min	Amps/µs max
С	500	400	200	150	100	150	50	500

Notes:

1. Current waveform in µs

- Peak pulse current rating (IPP) is repetitive and guaranteed for the life of the product.

2. Voltage waveform in µs

IPP ratings applicable over temperature range of -40°C to +85°C
The device must initially be in thermal equilibrium with -40°C < TJ < +150°C

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Thermal Considerations

Package	Symbol	Parameter	Value	Unit
DO-214AA	TJ	Operating Junction Temperature Range	- 40 to +150	°C
	Ts	Storage Temperature Range	- 40 to +150	°C
	Reja	Thermal Resistance: Junction to Ambient	90	°C/W

Characteristic Curves





pp tr=rise time to peak value lpp – Peak Pulse Current - % I 100 td=decav time to half value Peak Value Waveform=tr×td

td

Half Value

Figure 2 - tr x td Pulse Waveform

50

0 tr



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Environmental Specifications

High Temp Voltage Blocking	80% Rated VDRM (VAC Peak) +125°C or +150°C, Lead Material Copper Alloy High Temp Voltage Blocking 504 or 1008 hrs. MIL-STD-750 (Method 1040) JEDEC, JESD22-A-101
Temp Cycing	-65°C to +150°C, 15 min. dwell, 10 up to 100 cycles.MIL-STD-750 (Method 1051) EIA/JEDEC, JESD22-A104
Biased Temp & Humidity	52 VDC (+85°C) 85%RH, 504 up to 1008 hrs. EIA/ JEDEC, JESD22-A-101
High Temp Storage	+150°C 1008 hrs. MIL-STD-750 (Method 1031) JEDEC, JESD22-A-101
Low Temp Storage	-65°C, 1008 hrs.
Thermal Shock	0°C to +100°C, 5 min. dwell, 10 sec. transfer, Thermal Shock 10 cycles. MIL-STD-750 (Method 1056) JEDEC, JESD22-A-106
Autoclave (Pressure Cooker Test)	+121°C, 100%RH, 2atm, 24 up to 168 hrs. EIA/Cooker Test) JEDEC, JESD22-A-102
Resistance to Solder Heat	+260°C, 30 secs. MIL-STD-750 (Method 2031
Moisture Sensitivity Level	85%RH, +85°C, 168 hrs., 3 reflow cycles Level (+260°C Peak). JEDEC-J-STD-020, Level 1

Physical Specifications

Lead Material	Copper Alloy
Terminal Finish	100% Matte-Tin Plated
Body Material	UL recognized epoxy meeting flammability classification 94V-0

Soldering Parameters



Reflow	Condition	Lead-free assembly		
	-Temperature Min (Ts(min))	+150°C		
Pre Heat	-Temperature Max (Ts(max))	+200°C		
	- Time (min to max) (Ts)	60 -180 Seconds		
Average Temp Ti	ramp up rate (Liquidus .) to peak	3°C/Second max		
Ts(max)	to T∟ - Ramp-up Rate	5°C/Second max		
Reflow	- Temperature (TL) (Liquidus)	217°C		
	- Time (min to max) (Ts)	60 -150 Seconds		
Peak Temperature (TP)		260 +0/-5°C		
Time with Tempera	thin 5°C of actual peak ature (TP)	30 Seconds Max		
Ramp-down Rate		6°C/Second Max		
Time 25	°C to peak Temperature (TP)	8 minutes Max		
Do not e	exceed	+260°C		

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Date code

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PXXXC

XXXX

Part Marking

Part Marking Code (Refer to Electrical Characteristics Table)

Part Numbering



Dimensions DO-214AA



Dimensione	Inc	hes	Millimeters		
Dimensions	Min	Max	Min	Max	
Α	0.130	0.156	3.30	3.95	
В	0.201	0.220	5.10	5.60	
С	0.077	0.087	1.95	2.20	
D	0.159	0.181	4.05	4.60	
E	0.030	0.063	0.76	1.60	
F	0.076	0.096	1.90	2.45	
G	0.002	0.008	0.05	0.20	
н	0.077	0.104	1.95	2.65	
К	0.006	0.016	0.15	0.41	

Packaging

Part Number	Component Package	Quantity	Packaging Option	Packaging Specification
Pxxx0SC	DO-214AA	2500	Tape & Reel -12mm/13″tape	EIA -481 - D

Tape and Reel Specifications



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For technical questions, contact: tech@unsemi.com.tw

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ROHS