

**PRODUCT:** TFT TOUCH MODULE

MODULE NO. : WKS43WV003-WCT

SUPPLIER: WKS Technology Co., LTD

**DATE:** Mar 3, 2019

# **SPECIFICATION**

Revision: 0.0

## WKS43WV003-WCT

This module uses ROHS material

This specification may change without prior notice in order to improve performance or quality. Please contact WKS R&D department for updated specification and product status before design for this product or release of this order.

WRITTEN BY	CHECKED BY	APPROVED BY
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# **REVISION RECORD**

REV NO.	REV DATE	CONTENTS	REMARKS
0.0	2019-04-03	First release	Preliminary



Version:0.0 Mar 3, 2019

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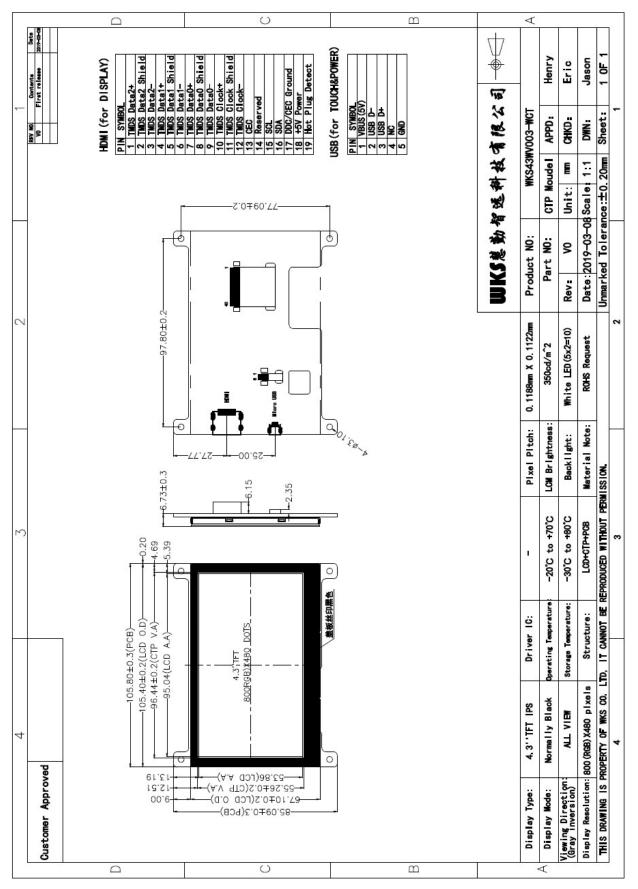


## 1, GENERAL INFORMATION

Item of general information		Contents		
LCD Display Size (Diagonal)		4.3		
Module Structure	LCD Displ	ay + CTP Touch + PCB	-	
LCD Display Type		TFT/IPS	-	
LCD Display Mode	Λ	ormally Black	-	
Recommended Viewing Direction		ALL VIEW	-	
Module size (W×H×T)	105	7.80×85.09×6.73	mm	
Active area (W×H)		95.04×53.86	тт	
Number of pixels (Resolution)	Ċ	800RGB×480	pixel	
Pixel pitch (W×H)	0	0.1188×0.1122	тт	
Color Pixel Arrangement		RGB Stripe	-	
	LCD Display	HDMI interface	-	
Module Interface Type	CTP Touch	USB interface	-	
	Win7/Win8	-		
System Support	Android/Linux (need to be configured first)		-	
Power Supply	Micro USB (5.0V)		-	
Module Power consumption	500(Max)		mA	
Color Numbers	16.7M		-	
Backlight Type		White LED	-	



## 2, EXTERNAL DIMENSIONS





# 3, ABSOLUTE MAXIMUM RATINGS

Parameter of absolute maximum ratings	Symbol	Min	Max	Unit
Operating temperature	Тор	-20	70	$^{\circ}\!C$
Storage temperature	Tst	-30	80	$^{\circ}\!C$
Humidity	RH	-	90%(Max 60°C)	RH

Note: Absolute maximum ratings mean the product can withstand short-term, not more than 120 hours. If the product is a long time to withstand these conditions, the life time would be shorter.

# 4, ELECTRICAL CHARACTERISTICS(DC CHARACTERISTICS)

Parameter of DC characteristics	Symbol	Min.	Typ.	Max.	Unit
PCB operating voltage	VUSB	4.8	5.0	_	V
LCD I/O operating voltage	VDD	3.0	3.3	3.6	V
Input voltage 'H' level	VIH	0.7*VDD	-	VDD	V
Input voltage 'L' level	VIL	VSS	-	0.3*VDD	V
Output voltage 'H' level	VOH	VDD-0.4	-	VDD	V
Output voltage 'L' level	VOL	VSS	-	VSS+0.4	V



# 5, CTP CHARACTERISTICS

Item of CTP characteristics	Specification	Unit	Remark
Panel Type	Glass Cover + Glass Sensor	-	-
Resolution	800  imes 480	pixel	-
Surface Hardness	<i>≥</i> 6H	-	-
Transparency	>82%	-	-
Driver IC	-	-	-
Interface Type	USB	-	-
Support Points	5 (Max)	-	-
Sampling Rate	20~100	Hz	-
Supply voltage	3.3	V	-



## 6, ELECTRO-OPTICAL CHARACTERISTICS

Item o electro-op character	otical	Symbol	Condition	Min.	Typ.	Max.	Unit	Remark	Note
Response	time	Tr+Tf		-	30	40	ms	FIG 1.	4
Contrast l	Ratio	CR	$egin{array}{c}  heta{=}0 \  extsf{arsigma}{=}0 \end{array}$	640	800	-	-	<i>FIG 2</i> .	1
Luminance un	iformity	<i>SWHITE</i>	<i>Ta=25℃</i>	-	80	-	%	<i>FIG 2.</i>	3
Surface Lum	inance	Lv		-	350	-	cd/m2	<i>FIG 2</i> .	2
	HZI.:	White x		0.309	0.313	0.315		FIG 2.	5
	White	White y		0.337	0.339	0.341	- - -		
		Red x		0.629	0.631	0.633			
CIE (x, y)	Red	Red y	$\theta = 0$	0.327	0.329	0.331			
chromaticity	G	Green x	$\emptyset=0$ $Ta=25^{\circ}C$	0.326	0.328	0.330			
	Green	Green y	1 <i>u</i> -25 C	0.546	0.548	0.550			
	DI	Blue x		0.134	0.136	0.138			
	Blue	Blue y	_	0.139	0.141	0.143			
	Ø=90(1	2 o'clock)		70	80	-	deg		
Viewing	Ø=270	(6 o'clock)	$CR \ge 10$	70	80	-	deg deg		6
angle range	Ø=0(3	o'clock)		70	80	-		<i>FIG 3</i> .	
	Ø=180	(9 o'clock)		70	80	-	deg		
NTSC ratio		-	-	-	50	-	%	-	-

*Note 1. Contrast Ratio(CR) is defined mathematically by the following formula. For more information see FIG 2.:* 

*Note 2.* Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

Lv=Average Surface Luminance with all white pixels (P1,P2,P3,P4,P5,P6,P7,P8,P9)

*Note 3.* The uniformity in surface luminance ( $\delta$ WHITE) is determined by measuring

luminance at each test position 1 through 9, and then dividing the maximum luminance of 9points luminance by minimum luminance of 9 points luminance. For more information see FIG 2.

 $\delta WHITE = \frac{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5, P6, P7, P8, P9)}{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5, P6, P7, P8, P9)}$ 

*Note 4.* Response time is the time required for the display to transition from White to black(Rise Time, Tr) and from black to white(Decay Time, Tf). For additional information see FIG 1.

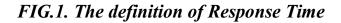
*Note 5. CIE* (*x*, *y*) *chromaticity*, *The x,y value is determined by screen active area position 5. For more information see FIG 2.* 

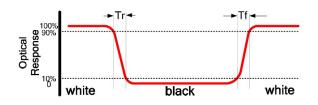
Note 6. Viewing angle is the angle at which the contrast ratio is greater than a specific value. For TFT module, the specific value of contrast ratio is 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

Note 7. For Viewing angle and response time testing, the testing data is base on

Autronic-Melchers's ConoScope. Series Instruments. For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing data is base on BM-7 photo detector.

*Note 8.* For TN type TFT transmissive module, Gray scale reverse occurs in the direction of panel viewing angle.





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#### FIG.2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity,

#### CIE (x, y) chromaticity

A : H/6 ; B : V/6 ;

*H*,*V* : *Active Area(AA) size* 

*Measurement instrument: BM-7; Light spot size=5mm, 350mm distance from the LCD surface to detector lens.* 

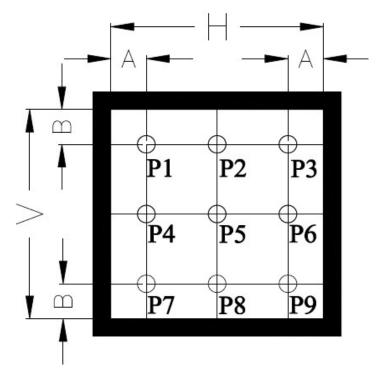
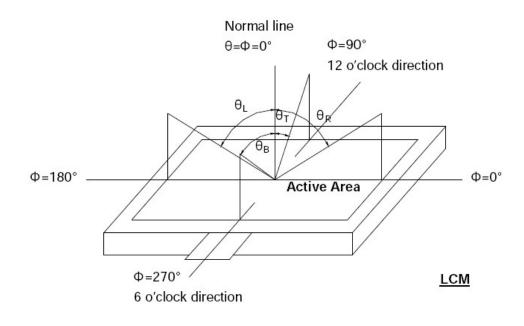


FIG.3. The definition of viewing angle





## 7, INTERFACE DESCRIPTION

#### A . **HDMI** Interface Description

<i>NO</i> .	Symbol	DESCRIPTION			
1	TMDS Data2+	Positive side of channel 2 TMDS low-voltage signal			
1		differential input pair			
2	TMDS Data2 Shield	Ground			
3	TMDS Data2-	Negative side of channel 2 TMDS low-voltage signal			
5		differential input pair			
4	TMDS Data1+	Positive side of channel 1 TMDS low-voltage signal			
7		differential input pair			
5	TMDS Data1 Shield	Ground			
6	TMDS Data1-	Negative side of channel 1 TMDS low-voltage signal			
0	TMDS Dalu1-	differential input pair			
7	TMDS Data0+	Positive side of channel 0 TMDS low-voltage signal			
/		differential input pair			
8	TMDS Data0 Shield	Ground			
9	TMDS Data0-	Negative side of channel 0 TMDS low-voltage signal			
,		differential input pair			
10	TMDS Clock+	Positive side of reference clock. TMDS low-voltage signal			
		differential input pair			
11	TMDS Clock Shield	Ground			
12	TMDS Clock-	Negative side of reference clock. TMDS low-voltage signal			
12		differential input pair			
13	CEC	No Connection			
14	Reserved(N.C.)	No Connection			
15	SCL	DDC SCL			
16	SDA	DDC SDA			
17	DDC/CEC Ground	Ground			
18	+5V Power	+5V Power			
19	Hot Plug Detect	Hot Plug Detect			

#### **B**, **USB** Interface Description

<i>NO</i> .	Symbol	DESCRIPTION
1	VUSB	USB Power
2	D-	USB Data-
3	D+	USB Data+
4	NC	No connection
5	GND	Power Ground

Application Note: Please connect the USB first, and then connect the HDMI interface.



## 8, LCD TIMING

Dermann of en	Course h e l		I Inii		
Parameter	Symbol	Min.	Тур.	Max.	Unit
PCLK frequency@ Frame rate=60Hz	PCLK	-	30	50	MHz
Horizontal display area	thd		800		PCLK
Horizontal period time	th	889	928	1143	PCLK
HSYNC Back Porch	thbp	-	88	-	PCLK
HSYNC Front Porch	thfp	1	40	255	PCLK
HSYNC Pulse Width	thw	1	48	255	PCLK
Vertical display area	tvd	480		Н	
VSYNC period time	tv	513	525	767	Н
VSYNC Back Porch	tvbp	-	32	-	Н
VSYNC Front Porch	tvfp	1	13	255	Н
VSYNC Pulse Width	tvw	3	3	255	Н



## 9, RELIABILITY TEST CONDITIONS

No.	Test Item	Test Condition
1	High Temperature Storage	80°C/120 hours
2	Low Temperature Storage	-30°C/120 hours
3	High Temperature Operating	70°C/120 hours
4	Low Temperature Operating	-20°C/120 hours
5	Temperature Cycle Storage	-20°C(30min.)~25(5min.)~70°C(30min.)×10cycles

## A. Inspection after test:

Inspection after 2~4 hours storage at room temperature, the sample shall be free from

defects:

- *▶* Air bubble in the LCD;
- ➢ Sealleak;
- > Non-display;
- Missing segments;
- Glass crack;
- Current is twice higher than initial value.

#### B, Remark:

- > The test samples should be applied to only one test item.
- Sample size for each test item is 5~10pcs.
- Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

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## 10, INSPECTION CRITERION

This specification is made to be used as the standard of acceptance/rejection criteria for TFT-LCD/IPS TFT-LCD module product, and this specification is applicable only in the case that the size of module equal to or exceed than 3.5 inch.

## 10.1 Sample plan

Sampling plan according to GB/T2828.1-2003/ISO 2859-1: 1999 and ANSI/ASQC

Z1.4-1993, normal level 2 and based on:

Major defect: AQL 0.65

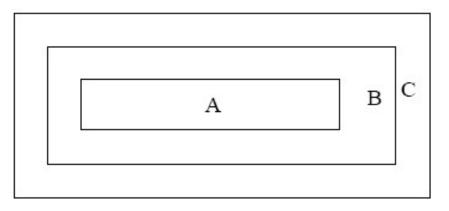
Minor defect: AQL 1.5

#### 10.2 Inspection condition

Viewing distance for cosmetic inspection is about 30cm with bare eyes, and under an environment of 20~40W light intensity, all directions for inspecting the sample should be within 45 °against perpendicular line. (Normal temperature 20~25 °C and normal humidity 60  $\pm 15\%$ RH )

## 10.3 Definition of Inspection Item.

## A. Definition of inspection zone in LCD.



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Zone A: character/Digit area

*Zone B: viewing area except Zone A (Zone A + Zone B=minimum Viewing area)* 

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig.1 Inspection zones in an LCD

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

#### **B**, Definition of some visual defect

	Because of losing all or part function, bad pixel dots appear bright and the
Bright dot	size is more than 50% of one dot in which LCD panel is displaying under
	black pattern.
Durch de t	Dots appear dark and unchanged in size in which LCD panel is displaying
Dark dot	under pure red, green, blue picture, or pure whiter picture.

## 10.4 Major Defect

Item No.	Items to be inspected	Inspection standard	Classification of defects
1	Functional defects	<ol> <li>No display</li> <li>Display abnormally</li> <li>Missing vertical, horizontal segment</li> <li>Short circuit</li> <li>Excess power consumption</li> <li>Backlight no lighting, flickering and abnormal lighting</li> </ol>	major
2	Missing	Missing component	
3	Outline dimension	Overall outline dimension beyond the drawing is not allowed	



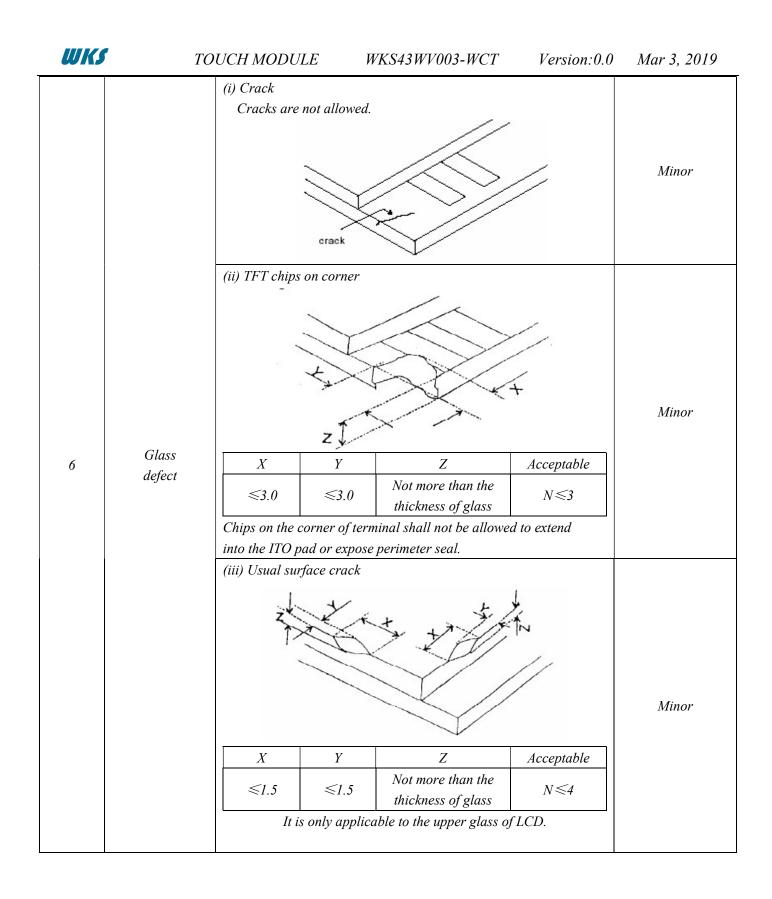
## 10.5 Minor Defect

Item	Items to be	Inspection standard						Classification					
No.	inspected										of defects		
			Zone Acceptable Qty										
				A+B									
					3.5~	7"	7~10.1"		>10.1"	С			
		Bright pixel dot			1		2		3				
	Bright dot	Dat		4		4	_	4	Ac				
1	/dark dot	2bright		0		0	0		Acceptable	Minor			
	defect	2dark		0		0		0	able				
		Total bi	right and	dark	5		6		7	(0			
			dots	1		1.0							
		Note: Minimum distance between defective dots is more than 5mm; Pixel dots' function is normal, but bright dots caused by foreign											
			0			0				-			
		material ar		easons a	ire jua	-	-		ect of 5.2	•			
	Dot defect $\downarrow y$ $\downarrow x$ $\Phi = (x+y)/2$	Zone Acceptable Qty											
		Size(mm)		A+B 3.5"~7" 7~10.1" >10.1"				С					
		$\Phi \leq$	50.2	Acceptabl					ceptable				
		Q ↓ ↓ ↓										Accu	
2			0.2<4	⊅≤0.5	0.5 4				5	6	Acceptable	Minor	
			$\phi >$	Φ>0.5				0 0		0			
		Note:											
		1. Minimum distance between defective dots is more than 5 mm;											
		2. The quantity of defect is zero in operating condition.											
3	Linear defect		Zone			4	. 11	0					
					Acceptable Qty								
		Size (mm)			A + B								
			Length	Width	3.5"~	~7"	7~	~10.1"	2	>10.1"	С	16	
			defect	defect	Ignore	W≤0.05	Accept	table	Acce	eptable	Ace	ceptable	$A_{\rm c}$
		0.05 <						6	Acceptable				
		$L \leq 5.0$	W≤0.1	4		5	ntabi						
		L>5.0	W>0.1	0			0		0	le			
								1					

				// 110 / 5	W V 00 <b>5-</b> WC.		<i>on</i> . 0.0	<i>intal 5, 2017</i>	
		5.4.1 Pola (i) Shiftin dimensior (ii) Incon allowed. 5.4.2 Dirt Dirt whic 5.4.3 Pola	10101 5, 2019						
			Zone		Acceptable A+B	Qty			
		Size(mm)		3.5"~7"	A+D 7~10.1"	с			
			) ≤0.2	3.3 ~/ Acceptable	Acceptable	>10.1" Acceptable			
		0.2 <	$\Phi \leq 0.5$	4	5	6	cept	Minor	
4	Polarizer defect	$\phi$	>0.5	0	0	0	Acceptable		
		or in the (ii)If the	operating polarizer or some Zone	scratch can b condition, ju r scratch can special angle 3.5"~7" Acceptable 4 0	udge by the l be seen only	inear defect o in non-oper ne following:	of 5.3.		
5	MURA	Using 3% ND filter, it's NG if it can be seen in R,G,B picture.							
	White/Black dot (MURA)	V 0.1,	Minor						

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## 10.6 Module Cosmetic Criteria

Item No.	Items to be inspected	Inspection Standard	Classification of defects		
1	Difference in Spec.	Not allowable	Major		
2	Pattern peeling	No substrate pattern peeling and floating	Major		
		No soldering missing	Major		
3	Soldering defects	No soldering bridge	Major		
		No cold soldering	Minor		
4	Resist flaw on PCB	Visible copper foil ( $\Phi 0.5$ mm or more) on substrate pattern is not allowed	Minor		
5	FPC gold finger	No dirt, breaking, oxidation lead to black	Major		
6	Backlight plastic frame	No deformation, crack, breaking, backlight positioning column breaking, obvious nick.	Minor		
7	Marking printing effect	No dark marking, incomplete, deformation lead to unable to judge	Minor		
8	Accretion of metallic Foreign matter	No accretion of metallic foreign matter (Not exceed $\Phi$ 0.2mm)	Minor		
9	Stain	No stain to spoil cosmetic badly	Minor		
10	Plate discoloring	No plate fading, rusting and discoloring	Minor		
	1. Lead parts	a. Soldering side of PCB Solder to form a 'Filet' all around the lead. Solder should not hide the lead form perfectly.	Minor		
		b. Components side(In case of 'Through Hole PCB') Solder to reach the Components side of PCB.	Minor		
11	2. Flat packages	Either 'Toe'(A) or 'Seal'(B)of the lead to be covered by "Filet". Lead form to be assume over Solder.	Minor		
11	3. Chips	$(3/2) H \ge h \ge (1/2) H$	Minor		
	4. Solder ball/Solder splash	a. The spacing between solder ball and the conductor or solder pad $h \ge 0.13$ mm. The diameter of solder ball $d \le 0.15$ mm.	Minor		
		b. The quantity of solder balls or solder splashes isn't beyond 5 in 600 mm2.	Minor		
		c. Solder balls/Solder splashes do not violate minimum electrical clearance.	Major		



## 11, PRODUCT PHOTOS

