

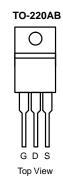
4P0407-VB TO220 Datasheet P-Channel 40-V (D-S) MOSFET

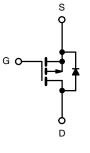
PRODUCT SUMMARY					
V _{DS} (V)	r _{DS(on)} (Ω)	I _D (A) ^a	Q _g (Typ.)		
- 40	0.0041 at V _{GS} = - 10 V	- 110	185 nC		

FEATURES

• TrenchFET[®] Power MOSFET







P-Channel MOSFET

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	- 40	V	
Gate-Source Voltage		V _{GS}	± 20	v
	T _C = 25 °C		- 110 ^a	
Continuous Drain Current (T 175 °C)	T _C = 70 °C		- 110 ^a	
Continuous Drain Current (T _J = 175 °C)	T _A = 25 °C	I _D	39 ^{b, c}	
	T _A = 70 °C		33 ^{b, c}	A
Pulsed Drain Current		I _{DM}	240	A
Continuous Source-Drain Diode Current	T _C = 25 °C		110	
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	10 ^{b, c}	
Avalanche Current	L = 0.1 mH	I _{AS}	75	
Single-Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	281	mJ
	T _C = 25 °C		375	
Maximum Dawar Dissinction	T _C = 70 °C	PD	262	w
Maximum Power Dissipation	T _A = 25 °C	ГD	15 ^{b, c}	vv
	T _A = 70 °C		10.5 ^{b, c}	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C
Soldering Recommendations (Peak Temperature		260		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	$t \le 10 s$	R _{thJA}	8	10	°C/W	
Maximum Junction-to-Case (Drain)	Steady State	R _{thJC}	0.33	0.4	0,00	

Notes:

a. Package limited.b. Surface Mounted on 1" x 1" FR4 board. c. t = 10 s.

d. Maximum under Steady State conditions is 40 °C/W.

SPECIFICATIONS $T_J = 25 \text{ °C}$, u	inless othei	rwise noted					
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_D = -250 \mu A$	- 40			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = - 250 μA		- 40		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	ι <u>μ</u> = - 200 μΑ		- 5.5			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \ \mu A$	- 2	- 3	- 4	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zara Cata Valtaga Drain Current	1	$V_{DS} = -40 \text{ V}, V_{GS} = 0 \text{ V}$			- 1		
Zero Gate Voltage Drain Current	IDSS	V_{DS} = - 40 V, V_{GS} = 0 V, T_{J} = 55 °C			- 10	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5$ V, $V_{GS} = -10$ V	- 120			Α	
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = - 10 V, I _D = - 20 A		0.0041		Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 20 A		75		S	
Dynamic ^b				•	•		
Input Capacitance	C _{iss}			11300		pF	
Output Capacitance	C _{oss}	V _{DS} = - 25 V, V _{GS} = 0 V, f = 1 MHz		1510			
Reverse Transfer Capacitance	C _{rss}			1000			
Total Gate Charge	Qg			185	280	nC	
Gate-Source Charge	Q _{gs}	V_{DS} = - 20 V, V_{GS} = - 10 V, I_{D} = - 110 A		48			
Gate-Drain Charge	Q _{gd}			42			
Gate Resistance	R _g	f = 1 MHz		4.0		Ω	
Turn-On Delay Time	t _{d(on)}			25	40		
Rise Time	t _r	V_{DD} = - 20 V, R _L = 0.18 Ω		290	440	- ns	
Turn-Off Delay Time	t _{d(off)}	${\rm I}_{\rm D}\cong$ - 110 A, ${\rm V}_{\rm GEN}$ = - 10 V, ${\rm R}_{\rm g}$ = 1 Ω		110	165		
Fall Time	t _f			35	55		
Drain-Source Body Diode Characteristic	S					1	
Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C			- 110	٨	
Pulse Diode Forward Current ^a	I _{SM}				- 240	A	
Body Diode Voltage	V _{SD}	I _S = - 20 A		- 0.8	- 1.5	V	
Body Diode Reverse Recovery Time	t _{rr}			70	105	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = - 20 A, di/dt = 100 A/μs, T _{.I} = 25 °C		130	200	nC	
Reverse Recovery Fall Time	t _a	$r_F = -20$ A, u/ul = 100 A/µs, $r_J = 25$ °C		37		ns	
Reverse Recovery Rise Time	t _b			33			

Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

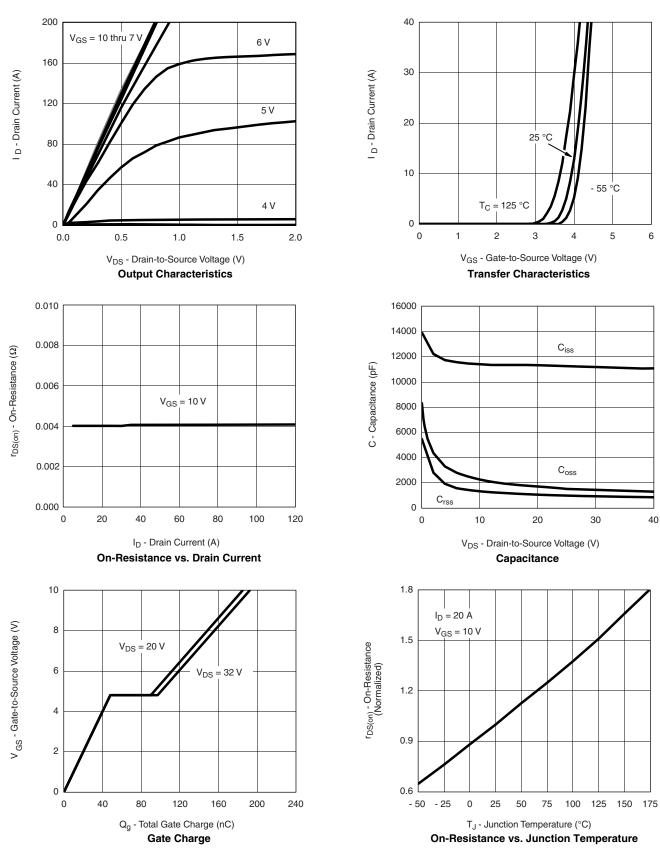
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

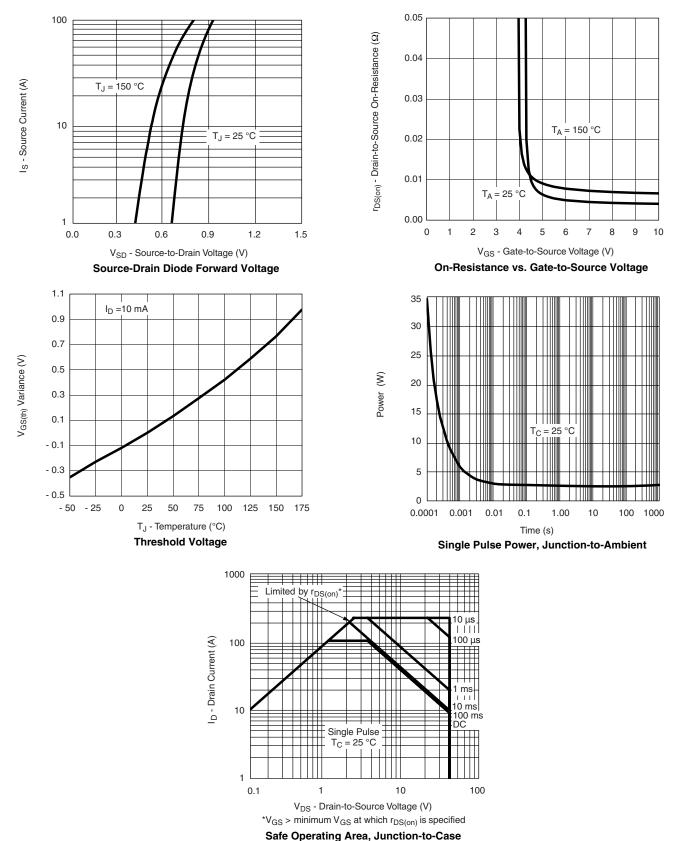
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

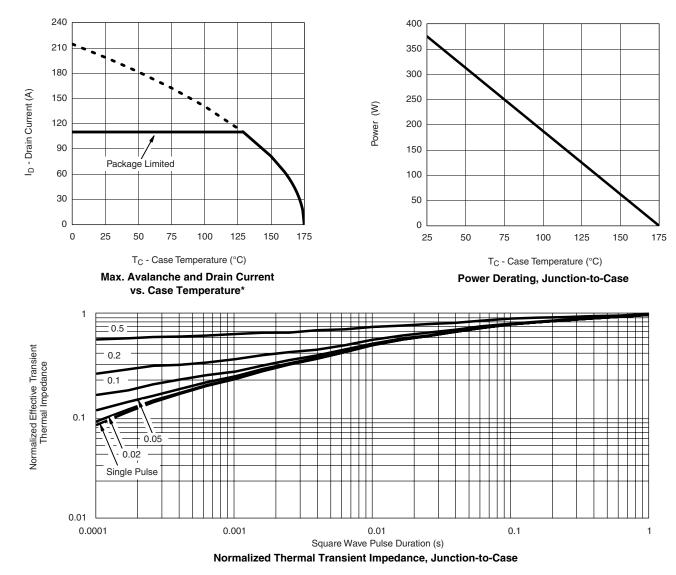






TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



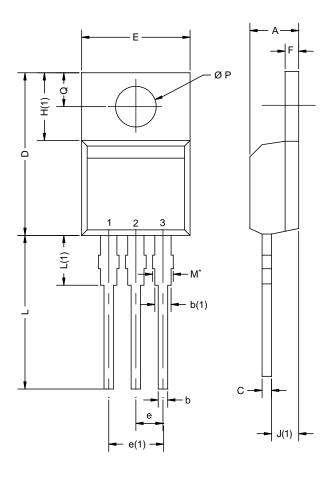


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

* The power dissipation P_D is based on $T_{J(max)} = 175$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



TO-220AB



	MILLIN	IETERS	INC	ICHES	
DIM.	MIN.	MAX.	MIN.	MAX.	
А	4.25	4.65	0.167	0.183	
b	0.69	1.01	0.027	0.040	
b(1)	1.20	1.73	0.047	0.068	
С	0.36	0.61	0.014	0.024	
D	14.85	15.49	0.585	0.610	
Е	10.04	10.51	0.395	0.414	
е	2.41	2.67	0.095	0.105	
e(1)	4.88	5.28	0.192	0.208	
F	1.14	1.40	0.045	0.055	
H(1)	6.09	6.48	0.240	0.255	
J(1)	2.41	2.92	0.095	0.115	
L	13.35	14.02	0.526	0.552	
L(1)	3.32	3.82	0.131	0.150	
ØΡ	3.54	3.94	0.139	0.155	
Q	2.60	3.00	0.102	0.118	
ECN: X12-0208-Rev. N, 08-Oct-12 DWG: 5471					

Notes

 * M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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