

2SK3362-01-VB Datasheet N-Channel 60-V (D-S) MOSFET

PRODUCT	SUMMARY	
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A) ^a
60	0.011 at V _{GS} = 10 V	60
00	0.013 at V _{GS} = 4.5 V	50

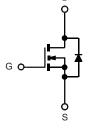
FEATURES

- 175 °C Junction Temperature
- TrenchFET[®] Power MOSFET
- Material categorization:

D







N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _C = 25	°C, unless other	vise noted)			
Parameter		Symbol	Limit	Unit	
Gate-Source Voltage		V _{GS}	± 20	V	
Continuous Durin Company (T. $= 175^{\circ}$ Colb	T _C = 25 °C	I _D	60		
Continuous Drain Current (T _J = 175 °C) ^b	T _C = 100 °C		50ª		
Pulsed Drain Current		I _{DM}	200	A	
Continuous Source Current (Diode Conduction)		۱ _S	50ª		
Avalanche Current	lanche Current		50		
Single Avalanche Energy (Duty Cycle ≤ 1 %)	L = 0.1 mH	E _{AS}	125	mJ	
Maximum Power Dissipation	T _C = 25 °C	Pn -	136	w	
	T _A = 25 °C 3 ^b , 8.3 ^b , c	3 ^b , 8.3 ^{b, c}	vv		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum lumation to Amelianta	$t \le 10 \text{ sec}$	R _{thJA}	15	18		
Maximum Junction-to-Ambient ^a	Steady State	™ thJA	40	50	°C/W	
Maximum Junction-to-Case		R _{thJC}	0.85	1.1		

Notes:

a. Package limited.

b. Surface mounted on 1" x 1" FR4 board.

c. $t \leq 10$ s.

Parameter Sym Static Vir Drain-Source Breakdown Voltage Vir Gate Threshold Voltage Vir Gate-Body Leakage Iir Zero Gate Voltage Drain Current Iir On-State Drain Currentb Iir Drain-Source On-State Resistanceb RDS	DS S(th) SS	V _{GS} = 0 V, I _D = 250 μ A V _{DS} = V _{GS} , I _D = 250 μ A V _{DS} = 0 V, V _{GS} = ± 20 V	Min. 60 1	Typ.ª	Max.	Unit
Drain-Source Breakdown Voltage Vreshold Gate Threshold Voltage VGS Gate-Body Leakage IGS Zero Gate Voltage Drain Current IDS On-State Drain Current ^b IDGS	S(th) SS	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$ $V_{DS} = 0 \ V, V_{GS} = \pm 20 \ V$				
Gate Threshold Voltage VGS Gate-Body Leakage IGS Zero Gate Voltage Drain Current IDS On-State Drain Current ^b ID(6)	S(th) SS	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$ $V_{DS} = 0 \ V, V_{GS} = \pm 20 \ V$				
Gate-Body Leakage IGS Zero Gate Voltage Drain Current IDS On-State Drain Current ^b IDS	SS	$V_{DS} = 0 V, V_{GS} = \pm 20 V$	1		1 1	V
Zero Gate Voltage Drain Current I _D		50 00			3	
On-State Drain Current ^b					± 100	nA
On-State Drain Current ^b	[$V_{DS} = 60 V, V_{GS} = 0 V$			1	
	50	$V_{DS} = 60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 125 \text{ °C}$			50	μA
	Γ	$V_{DS} = 60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 175 ^{\circ}\text{C}$			250	
	on)	V _{DS} = 5 V, V _{GS} = 10 V	60			А
Drain-Source On-State Resistance ^b		V _{GS} = 10 V, I _D = 20 A		0.011		
Drain-Source On-State Resistance TDS	Γ	V_{GS} = 10 V, I_{D} = 20 A, T_{J} = 125 °C		0.014		0
	S(on)	V _{GS} = 10 V, I _D = 20 A, T _J = 175 °C		0.018		
	F	V _{GS} = 4.5 V, I _D = 15 A		0.013		-Ω S pF
Forward Transconductance ^b 9	fs	V _{DS} = 15 V, I _D = 20 A		60		S
Dynamic						
Input Capacitance C _i	ss			4200		
Output Capacitance C _o	oss	V_{GS} = 0 V, V_{DS} = 25 V, f = 1 MHz		570		pF
Reverse Transfer Capacitance C _r	ss			325		
Total Gate Charge ^c Q	g			47		
Gate-Source Charge ^c Q	gs	V _{DS} = 30 V, V _{GS} = 10 V, I _D = 50 A		10		nC
Gate-Drain Charge ^c Q	gd			12		
Turn-On Delay Time ^c t _{d(c}	on)			10	20	
Rise Time ^c t		V_{DD} = 30 V, R_L = 0.6 Ω		15	25	ns
Turn-Off Delay Time ^c t _{d(c}	off)	$I_D {\cong} 50$ A, V_{GEN} = 10 V, R_g = 2.5 Ω		35	50	
Fall Time ^c t	f			20	30	
Source-Drain Diode Ratings and Characteri	stics (1	Γ _C = 25 °C)				
Pulsed Current I _S					60	А
Diode Forward Voltage V _s						
Reverse Recovery Time t _r	SD	I _F = 20 A, V _{GS} = 0 V		1	1.5	V

Notes:

a. For design aid only; not subject to production testing.

b. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

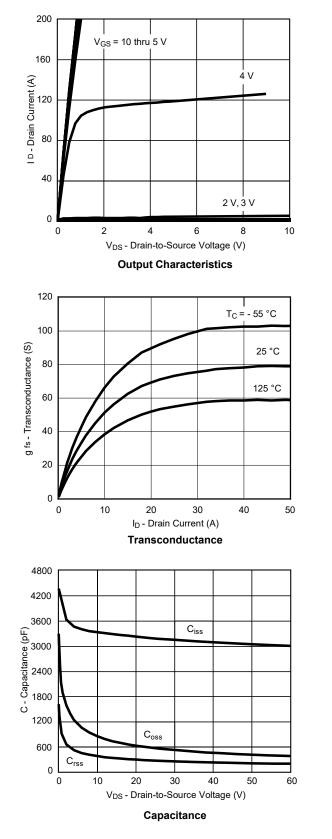
c. Independent of operating temperature.

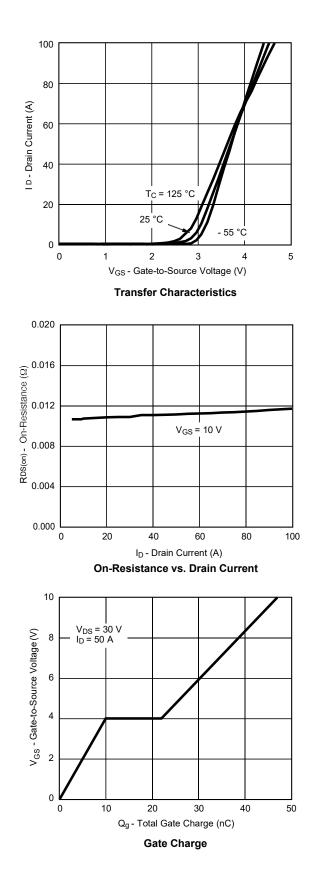
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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TYPICAL CHARACTERISTICS (25 °C unless noted)

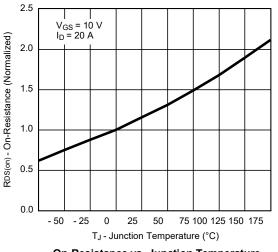




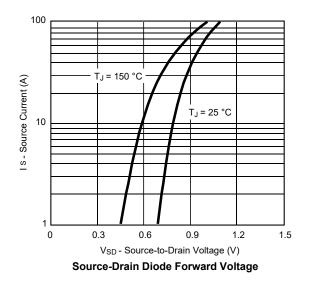
服务热线:400-655-8788



TYPICAL CHARACTERISTICS (25 °C unless noted)

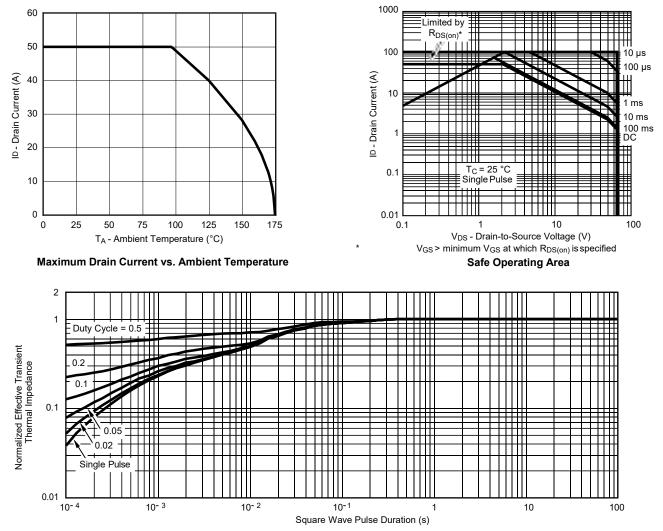


On-Resistance vs. Junction Temperature



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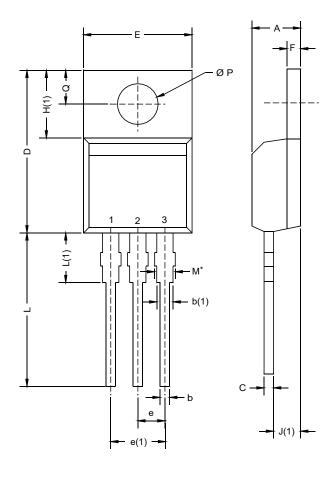
THERMAL RATINGS



Normalized Thermal Transient Impedance, Junction-to-Case



TO-220AB



DIM.	MILLIM	ETERS	INCHES		
	MIN.	MAX.	MIN.	MAX.	
А	4.24	4.65	0.167	0.183	
b	0.69	1.02	0.027	0.040	
b(1)	1.14	1.78	0.045	0.070	
С	0.36	0.61	0.014	0.024	
D	14.33	15.85	0.564	0.624	
Е	9.96	10.52	0.392	0.414	
е	2.41	2.67	0.095	0.105	
e(1)	4.88	5.28	0.192	0.208	
F	1.14	1.40	0.045	0.055	
H(1)	6.10	6.71	0.240	0.264	
J(1)	2.41	2.92	0.095	0.115	
L	13.36	14.40	0.526	0.567	
L(1)	3.33	4.04	0.131	0.159	
ØР	3.53	3.94	0.139	0.155	
Q	2.54	3.00	0.100	0.118	
ECN: X15- DWG: 603	0364-Rev. C, 1	14-Dec-15			

Note

• M* = 0.052 inches to 0.064 inches (dimension including protrusion), heatsink hole for HVM



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