

0.154(3.9)

0.130(3.3)

0.102(2.6)

0.084(2.13)

Case: SMB (DO-214AA)

0.187(4.75)

0.163(4.15)

0.217(5.5)

0.197(5.0)

0.008(0.20)

Dimensions in inches and (millimeters)

0.087(2.2)

0.071(1.8)

1

0.012(0.3)

0.004(0.1)

0.057(1.45)

0.033(0.85)

Features

- · Deally Suited for Automatic Assembly
- Low Power Loss, High Efficiency
- For Use in Low Voltage Application
- Plastic Case Material has UL Flammability Classication Rating 94V-0

Mechanical Data

- · Case: Molded plastic SMB
- Terminals: Plated leads solderable per MIL-STD-750,Method 2026 guaranteed
- Polarity:Cathode Band or Cathode Notch
- Mounting Position: Any
- Making: Type Number

Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load

For capacitive load derate current by 20%

Type Number	SYMBOL	FR2A	FR2B	FR2D	FR2G	FR2J	FR2K	FR2M	Unit
Maximum Recurrent Peak Reverse Voltage	VRRM	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	VRMS	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	VDC	50	100	200	400	600	800	1000	V
Average Rectified Output Current @T∟ =90 °C	lf(AV)	2.0							А
Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	IFSM	60							A
Forward Voltage @IF=2.0A	VFM	1.3							V
Peak Reverse Current @Ta =25 °C	5.0 I _R 100							uA	
At Rated DC Blocking Voltage @T _A =125 $^{\circ}$ C									
I ² t Rating for Fusing (t < 8.3ms)	²t	14.94							A ² s
Maximum Reverse Recovery Time(Note 1)	Trr	150 250 500				ns			
Typical Junction Capacitance (Note 2)	С	10							рF
Typical Thermal Resistance Junction to Ambient	Rθ JA	60							°C/W
Operating Temperature Range	ТJ	-55 to+150							°C
Storage Temperature Range	Tstg	-55 to +150							°C

Note:

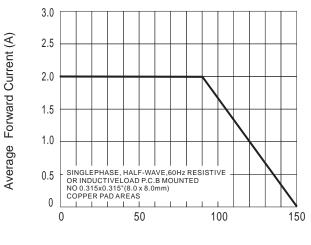
1. Reverse Recovery Test Conditions:IF=0.5A,IR=1.0A,IRR=0.25A.

2. Measured at 1.0 MHz and Applied reverse Voltage of 4.0V D.C

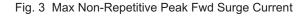


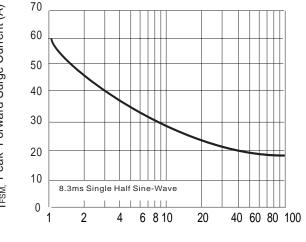
Fig. 2 Typ. Forward Characteristics

Fig. 1 Forward Current Derating Curve

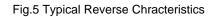


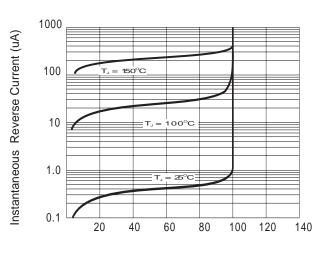
T_L Lead Temperature(°C)





Number Of Cycles At 60 Hz





Percent Of Rated Peak Reverse Voltage (%)

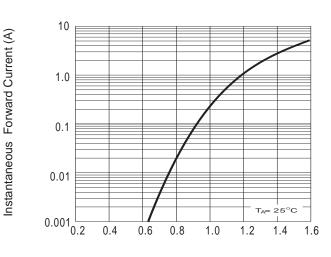
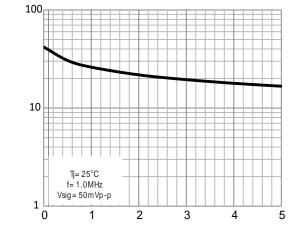


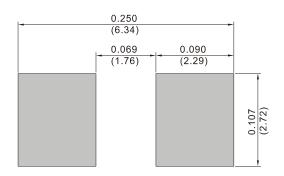


Fig.4 Typical Junction Capacitance



V_R,Reverse Voltage (V)

Fig.6 Mounting PAD Layout



I_{FSM,} Peak Forward Surge Current (A)

Capacitance, (pF)



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