VS-FC270SA20

Vishay Semiconductors



SOT-227 Power Module Single Switch - Power MOSFET, 270 A



PRIMARY CHARACTERISTICS				
V _{DSS} 200 V				
R _{DS(on)}	3.3 mΩ			
Ι _D	219 A at 90 °C			
Type Modules - MOSFET				
Package	SOT-227			

FEATURES

- I_D = 287 A, T_C = 25 °C
- ThunderFET power MOSFET
- Reduced switching and conduction losses
- Maximum 175 °C junction temperature
- UL approved file E78996
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- DC/DC conversions
- Motor drives switch
- DC/AC inverter
- Power supplies
 - Uninterruptible power supplies
 - AC/DC switchmode power supplies
 - Solar micro inverter

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
MOSFET						
Drain to source voltage	V _{DSS}		200	V		
		T _C = 25 °C	287			
Continuous drain current, $V_{GS at}$ 10 V	ID	T _C = 90 °C	219	А		
Pulsed drain current	I _{DM} ⁽¹⁾		680			
Power dissipation	PD	T _C = 25 °C	937	W		
Gate to source voltage	V _{GS}		± 20	V		
Single pulse avalanche energy ⁽²⁾	E _{AS}	T _C = 25 °C, L = 0.1 mH, V _{GS} = 10 V	650	mJ		
Avalanche current	I _{AS}	$V_{\rm C} = 25$ C, $L = 0.1$ IIIH, $V_{\rm GS} = 10$ V	180	А		
MODULE						
Operating junction temperature range	TJ		-55 to +175	0°		
Operating storage temperature range	T _{Stg}		-40 to +150	U		
Insulation voltage (RMS)	VISOL	Any terminal to case, t = 1 min	2500	V		

Notes

⁽¹⁾ Limited at max. junction temperature

⁽²⁾ Duty cycle \leq 1 %

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COMPLIANT



THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Operating junction temperature range		TJ		-55	-	175	°C
Operating storage temperature range		T _{Stg}		-40	-	150	U
Junction to case	MOSFET	R _{thJC}		-	-	0.16	°C/W
Case to heatsink	Module	R _{thCS}	Flat, greased surface	-	0.1	-	0/10
Weight				-	30	-	g
Mounting torque			Torque to terminal	-	-	1.1 (9.7)	Nm (lbf. in)
			Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf. in)
Case style				SOT-227			

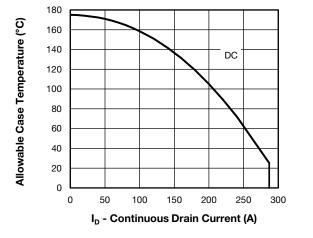
ELECTRICAL CHARACTERISTI	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Drain to source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0 V, I_D = 1.0 mA$	200	-	-	V
Breakdown voltage temperature coefficient	$\Delta V_{(BR)DSS} / \Delta T_J$	Reference to 25 °C, $I_D = 1.0$ mA	-	0.16	-	V/°C
Static drain to source on-resistance	R _{DS(on)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 200 \text{ A}$	-	3.3	4.7	mΩ
Gate threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 1.0 \text{ mA}$	1.8	3.16	4.3	V
Forward transconductance	g _{fs}	V _{DS} = 15 V, I _D = 100 A, V _{GS} = 10 V	-	270	-	S
		V _{DS} = 200 V, V _{GS} = 0 V	-	0.5	10	μA
Drain to source leakage current	I _{DSS}	$V_{DS} = 200 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 150 ^{\circ}\text{C}$	-	160	-	
Gate to source leakage	I _{GSS}	$V_{GS} = \pm 20 V$	-	-	± 200	nA
Total gate charge	Q_g $I_D = 120 \text{ A}$		-	250	-	
Gate to source charge	Q _{gs}	V _{DS} = 100 V	-	68	-	- nC
Gate to drain ("Miller") charge	Q _{gd}			70	-	
Turn-on delay time	t _{d(on)}	V = 100 V	-	76	-	
Rise time	$V_{DD} = 100 V$ t_r $I_D = 100 A$		-	212	-	
Turn-off delay time	t _{d(off)}	$t_{d(off)}$ $R_g = 1 \Omega$		134	-	ns
Fall time	t _f	V _{GS} = 10 V	-	118	-	
Input capacitance	C _{iss}	V _{GS} = 0 V	-	16.5	-	
Output capacitance	C _{oss}	$V_{\rm OSS} = 100 \text{ V}$ $V_{\rm DS} = 100 \text{ V}$		1.0	-	nF
Reverse transfer capacitance	C _{rss}	f = 1 MHz	-	0.8	-	
Temperature coefficient of threshold voltage	$\Delta V_{GE(th)} / \Delta T_J$	V _{DS} = V _{GS} , I _D = 1.0 mA (25 °C to 125 °C)	-	9.2	-	mV/°

SOURCE-DRAIN RATINGS AND CHARACTERISTICS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Continuous source current (body diode)	Is		-	-	287	
Pulsed source current (body diode)	I _{SM}	MOSFET symbol showing the integral reverse p-n junction diode	-	-	680	A
Diode forward voltage V _{SD}		I _S = 200 A, V _{GS} = 0 V	-	0.93	1.23	V
Reverse recovery time	t _{rr}		-	210	-	ns
Reverse recovery charge	Q _{rr}	$\begin{array}{c c} T_{J} = 25 \ ^{\circ}C, \ I_{F} = I_{S} = 50 \ A, \\ \hline dI/dt = 100 \ A/\mu s, \ V_{R} = 100 \ V \\ \hline & - \\ 15.7 \ - \\ \end{array}$		1646	-	nC
Reverse recovery current	I _{RM}			-	А	

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Fig. 1 - Maximum Continuous Drain Current vs. Case Temperature

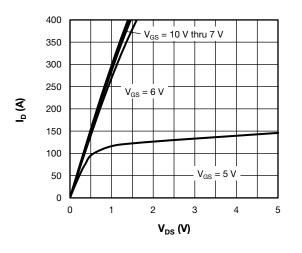


Fig. 2 - Typical Drain to Source Current Output Characteristics at $T_J = 125^\circ\text{C}$

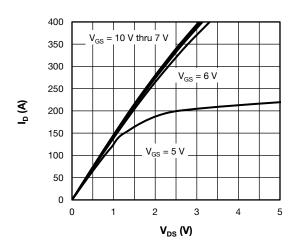


Fig. 3 - Typical Drain to Source Current Output Characteristics at $T_J = 125^\circ\text{C}$

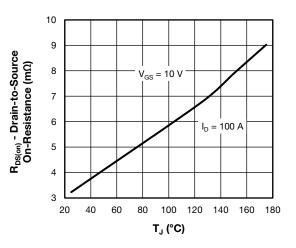


Fig. 4 - Typical Drain-to-Source On-Resistance vs. Temperature

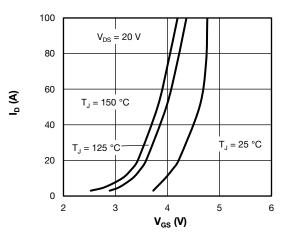


Fig. 5 - Typical Transfer Characteristics

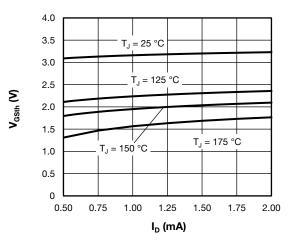
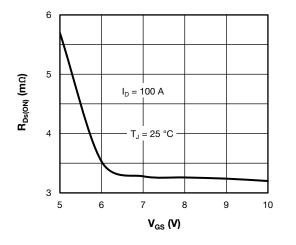


Fig. 6 - Typical Gate Threshold Voltage Characteristics

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Fig. 7 - Typical Drain - State Resistance vs. Gate to Source Voltage

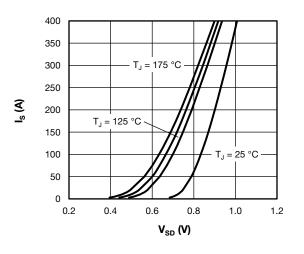


Fig. 8 - Typical Body Diode Source-to-Drain Current Characteristics

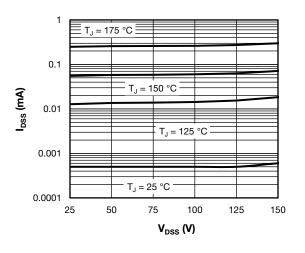


Fig. 9 - Typical Zero Gate Voltage Drain Current

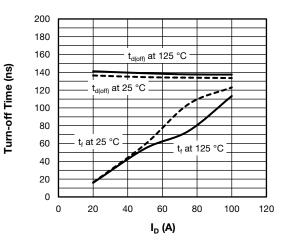


Fig. 10 - Typical Turn-Off Switching Time vs. I_D, V_DD = 100 V, R_g = 1.0 $\Omega,$ V_GS = \pm 10 V, L = 500 μH

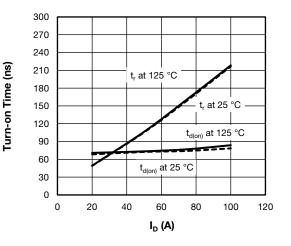


Fig. 11 - Typical Turn-On Switching Time vs. ID, $V_{DD} = 100 \text{ V}, \text{ R}_{g} = 1.0 \Omega, \text{ V}_{GS} = \pm 10 \text{ V}, \text{ L} = 500 \mu\text{H}$

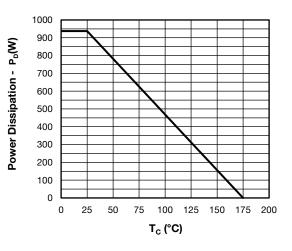


Fig. 12 - Power Dissipation Curve

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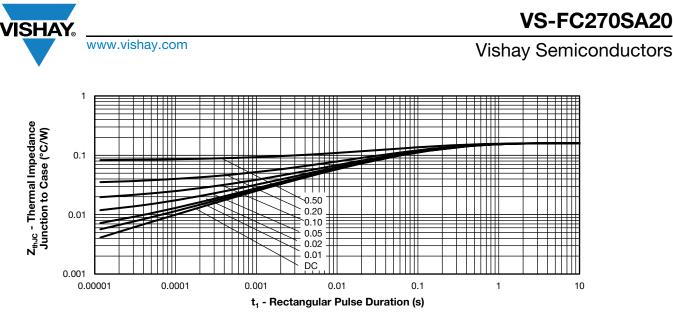


Fig. 13 - Maximum Thermal Impedance Junction-to-Case Characteristics

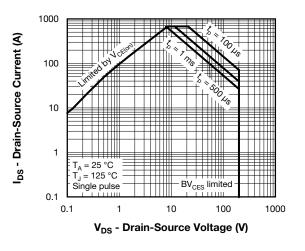
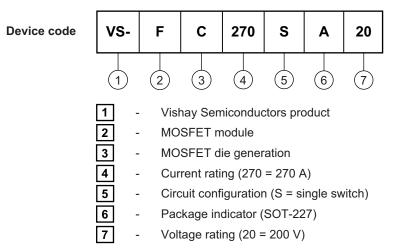


Fig. 14 - Safe Operating Area

ORDERING INFORMATION TABLE



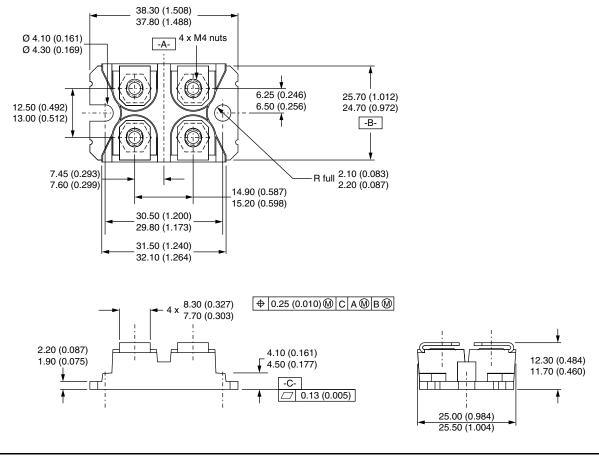


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CIRCUIT CONFIGURATION				
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING		
Single switch	S	Lead Assignment (S) $(D)(G)$		

DIMENSIONS in millimeters



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SOT-227 Generation 2

DIMENSIONS in millimeters (inches)



Note

• Controlling dimension: millimeter



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